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Date: Feb. 4, 2000TYPED NAME Daniel LuchSIGNED Daniel Luch

Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

This is a request for filing a Continuation-in-part patent application in the name of Daniel Luch (Inventor) for Substrate Structures For Integrated Series Connected Photovoltaic Arrays And Process Of Manufacture Of Such Arrays. This application is a Continuation-in-Part application of application Ser. No. 08/967,962, filed Nov 12, 1997 which is a Continuation-in-Part of application Ser. No. 08/683,061, filed July 16, 1996, now U.S. Patent 5,735,966 which was a Continuation-in-Part of application Ser. No. 08/441,552, filed May 15, 1995, now U.S. Patent No. 5,547,516.

1. (a) ☐ Enclosed is a new application.
 - (b) ☒ Enclosed is a continuation-in-part application.
 - (c) ☐ Enclosed is a copy of the prior application.
 2. (a) ☒ Enclosed is a new Declaration.
 - (b) ☐ Enclosed is a copy of the prior executed Declaration as originally filed.
 - (c) ☐ Enclosed is a Combined Declaration/Power of Attorney.
 3. (a) ☒ Enclosed is a Small Entity Affidavit.
 - (b) ☐ A Small Entity Affidavit is of record in the prior application.
 4. ☐ The filing fee is calculated below:
- Claims as filed in the prior application, less any claims canceled by amendment below:

| FOR: | (Col. 1) | (Col. 2) | SMALL ENTITY | | OR | OTHER THAN A SMALL ENTITY | |
|--|------------------|-----------|--------------|---------------|----------|---------------------------|-------------|
| | NO. FILED | NO. EXTRA | RATE | FEE | | RATE | FEE |
| BASIC FEE | | | | \$345 | OR | | \$690 |
| TOTAL CLAIMS | <u>1</u> -20 = * | | x 9 = | \$ <u>0</u> | OR | x 18 = | \$ <u>0</u> |
| INDEP CLAIMS | <u>1</u> -3 = * | | x 39 = | \$ <u>0</u> | OR | x 78 = | \$ <u>0</u> |
| [] MULTIPLE DEPENDENT CLAIM PRESENTED | | | +130 = | \$ <u>0</u> | OR | +260 = | \$ <u>0</u> |
| *If the difference in Col. 1 is less than zero, enter "0" in Col. 2. | | | TOTAL | \$ <u>345</u> | OR TOTAL | | \$ <u>0</u> |

5. Our check in the amount of \$ 345.00 is enclosed.
6. A Declaration for Patent Application is enclosed.
7. A Petition and fee payment for a two month extension of time is enclosed.
8. Also enclosed is a Small Entity Status Declaration Under 37 CFR 1.9(c) and (f) and 1.27(b).
9. Enclosed are 26 sheets of informal drawing.

Address all future communications to

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Phone: 408-779-1465

Respectfully submitted,

 Feb 4, 00

Daniel Luch

Applicant or Patentee: Daniel Luch
Serial or Patent No.: To Be Assigned
Filed or Issued:

For: SUBSTRATE STRUCTURES FOR INTEGRATED SERIES CONNECTED PHOTOVOLTAIC
ARRAYS AND PROCESS OF MANUFACTURE OF SUCH ARRAYS

**VERIFIED STATEMENT (DECLARATION) CLAIMING SMALL ENTITY
STATUS (37 CFR 1.9(c) and (f) and 1.27(b)) - INDEPENDENT INVENTOR**

As a below named inventor, I hereby declare that I qualify as an independent inventor as defined in 37 CFR 1.9(c) purposes of paying reduced fees under Section 41(a) and (b) of Title 35, United States Code, to the Patent and Trademark Office with regard to the invention entitled SUBSTRATE STRUCTURES FOR INTEGRATED SERIES CONNECTED PHOTOVOLTAIC ARRAYS AND PROCESS OF MANUFACTURE OF SUCH ARRAYS described in

- ☒ the specification filed herewith
☐ application serial no. _____, filed _____
☐ patent no. _____, issued _____

I have not assigned, granted, conveyed or licensed and am under no obligation under contract or law to assign, grant, convey or license, any rights in the invention to any person who could not be classified as an independent inventor under 37 CFR 1.9(c) if that person had made the invention, or to any concern which would not qualify as a small business concern under 37 CFR 1.9(d) or a nonprofit organization under 37 CFR 1.9(e).

Each person, concern or organization to which I have assigned, granted, conveyed, or licensed or am under an obligation under contract or law to assign, grant, convey, or license any rights in the invention is listed below:

- ☒ no such person, concern, or organization
☐ person, concerns or organizations listed below*

*NOTE: Separate verified statements are required from each named person, concern or organization having rights to the invention availing to their status as small entities. (37 CFR 1.27)

FULL NAME _____
ADDRESS _____

☐ Individual ☐ Small Business Concern ☐ Nonprofit Organization

NAME: _____
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NAME: _____
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I acknowledge the duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate. (37 CFR 1.28(b)).

I hereby declare that all statements made herein of my own knowledge are true and that all statements made in good faith and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application for any patent issuing thereon, or any patent to which this verified statement is directed.

Daniel Luch
NAME OF INVENTOR

Signature of Inventor

02/04/00
Date

SPECIFICATION

TO ALL WHOM IT MAY CONCERN:

BE IT KNOWN, that DANIEL LUCH, a citizen of the United States of America, residing at 17161 Copper Hill Drive, Morgan Hill, California, 95037, U.S.A., has invented certain new and useful improvements in

SUBSTRATE STRUCTURES FOR INTEGRATED SERIES
CONNECTED PHOTOVOLTAIC ARRAYS AND PROCESS OF
MANUFACTURE OF SUCH ARRAYS

of which the following is a specification

SUBSTRATE STRUCTURES FOR INTEGRATED SERIES CONNECTED
PHOTOVOLTAIC
ARRAYS AND PROCESS OF MANUFACTURE OF SUCH ARRAYS

Inventor: Daniel Luch

This application is a Continuation-in-Part of application Ser. No. 08/967,962 filed Nov 12, 1997 which is a Continuation-in-Part of application Ser. No. 08/683,061, filed July 16, 1996, now U.S. Pat. No. 5,735,966 which was a Continuation-in-Part of application Ser. No. 08/441,552, filed May 15, 1995, now U.S. Pat. No. 5,547,516.

5. BACKGROUND OF THE INVENTION

Photovoltaic cells have developed according to two distinct methods. The initial operational cells employed a matrix of single crystal silicon appropriately doped to produce a planar p-n junction. An intrinsic electric field established at the p-n junction produces a voltage by directing solar photon produced holes and free electrons in opposite directions. Despite good conversion efficiencies and long-term reliability, widespread energy collection using single-crystal silicon cells is thwarted by the exceptionally high cost of single crystal silicon material and interconnection processing.

15. A second approach to produce photovoltaic cells is by depositing thin photovoltaic semiconductor films on a supporting substrate. These thin semiconductor films are normally less than 30 micrometers thick and typically .05 to 5 micrometers thick. Material requirements are minimized and technologies can be proposed for mass production. The thin film structures can be designed according to doped homojunction technology such as that involving silicon films, or
20. can employ heterojunction approaches such as those using CdTe or chalcopyrite materials.

Despite significant improvements in individual cell conversion efficiencies for both single crystal and thin film approaches, photovoltaic energy collection has been generally restricted to applications having low power requirements. One factor impeding development of bulk power systems is the problem of economically

5. collecting the energy from an extensive collection surface. Photovoltaic cells can be described as high current, low voltage devices. Typically individual cell voltage is less than one volt. The current component is a substantial characteristic of the power generated. Efficient energy collection from an expansive surface must minimize resistive losses associated with the high current characteristic. A way to

10. minimize resistive losses is to reduce the size of individual cells and connect them in series. Thus, voltage is stepped through each cell while current and associated resistive losses are minimized.

It is readily recognized that making effective, durable series connections among multiple small cells can be laborious, difficult and expensive. In order to approach

15. economical mass production of series connected arrays of individual cells, a number of factors must be considered in addition to the type of photovoltaic materials chosen. These include the substrate employed and the process envisioned. Since thin films can be deposited over expansive areas, thin film technologies offer additional opportunities for mass production of interconnected arrays compared to

20. inherently small, discrete single crystal silicon cells. Thus a number of U.S. Patents have issued proposing designs and processes to achieve series interconnections among the thin film photovoltaic cells. Many of these technologies comprise deposition of photovoltaic thin films on glass substrates followed by scribing to form smaller area individual cells. Multiple steps then follow to electrically connect the

25. individual cells in series array. Examples of these proposed processes are presented in U.S. patents 4,443,651, 4,724,011, and 4,769,086 to Swartz, Turner et

al and Tanner et al. respectively. While expanding the opportunities for mass production of interconnected cell arrays compared with single crystal silicon approaches, glass substrates must inherently be processed on an individual batch basis.

5. More recently, developers have explored depositing wide area films using continuous roll-to-roll processing. This technology generally involves depositing thin films of photovoltaic material onto a continuously moving web. However, a challenge still remains regarding subdividing the expansive films into individual cells followed by interconnecting into a series connected array. For example, U.S. Patents 4,965,655 to Grimmer et. al. and U.S. Patent 4,697,041 to Okamiwa teach processes requiring expensive laser scribing and interconnections achieved with laser heat staking. In addition, these two references teach a substrate of thin vacuum deposited metal on films of relatively expensive polymers. The electrical resistance of thin vacuum metallized layers significantly limits the active area of the individual interconnected cells.
10. 15.

- It has become well known in the art that the efficiencies of certain promising thin film photovoltaic junctions can be substantially increased by high temperature treatments. These treatments involve temperatures at which even the most heat resistant plastics suffer rapid deterioration, thereby requiring either ceramic, glass, or metal substrates to support the thin film junctions. Use of a glass or ceramic substrates generally restricts one to batch processing and handling difficulty. Use of a metal foil as a substrate allows continuous roll-to-roll processing. However, despite the fact that use of a metal foil allows high temperature processing in roll-to-roll fashion, the subsequent interconnection of individual cells effectively in an interconnected array has proven difficult, in part because the metal foil substrate is electrically conducting.
20. 25.

U.S. Patent 4,747,618 to Nath et al. teaches a design and process to achieve interconnected arrays using roll-to-roll processing of a metal web substrate such as

stainless steel. The process includes multiple operations of cutting, selective deposition, and riveting. These operations add considerably to the final interconnected array cost.

- U.S. Patent 5,385,848 to Grimmer teaches roll-to-roll methods to achieve
5. integrated series connections of adjacent thin film photovoltaic cells supported on an electrically conductive metal substrate. The process includes mechanical or chemical etch removal of a portion of the photovoltaic semiconductor and transparent top electrode to expose a portion of the electrically conductive metal substrate. The exposed metal serves as a contact area for interconnecting adjacent
10. cells. These material removal techniques are troublesome for a number of reasons. First, many of the chemical elements involved in the best photovoltaic semiconductors are expensive and environmentally unfriendly. This removal subsequent to controlled deposition involves containment, dust and dirt collection and disposal, and possible cell contamination. This is not only wasteful but
15. considerably adds to expense. Secondly, the removal processes are difficult to control dimensionally. Thus a significant amount of the valuable photovoltaic semiconductor is lost to the removal process. Ultimate module efficiencies are further compromised in that the spacing between adjacent cells grows, thereby reducing the effective active collector area for a given module area.
20. Thus there remains a need for an inexpensive manufacturing process which allows high heat treatment for thin film photovoltaic junctions while also offering unique means to achieve effective integrated series connections.

- In a somewhat removed segment of technology, a number of electrically
25. conductive fillers have been used to produce electrically conductive polymeric materials. This technology generally involves mixing of the conductive filler into the polymer resin prior to fabrication of the material into its final shape. Conductive fillers typically consist of high aspect ratio particles such as metal fibers, metal

flakes, or highly structured carbon blacks, with the choice based on a number of cost/performance considerations.

Electrically conductive resins have been used as bulk thermoplastic compositions, or formulated into paints. Their development has been spurred in large part by electromagnetic radiation shielding and static discharge requirements for plastic components used in the electronics industry. Other known applications include resistive heating fibers and battery components.

In yet another separate technological segment, electroplating on plastic substrates has been employed to achieve decorative effects on items such as knobs, cosmetic closures, faucets, and automotive trim. Electrodeposits have substantially better durability than other forms of metal coatings such vacuum metallized deposits. Electrodeposited films can be quickly deposited to typical thicknesses of from 12 micrometers to 120 micrometers. In contrast, deposition via vacuum processing is much slower and thicknesses are typically 0.25 micrometers to 2.5 micrometers. Therefore, electrodeposits are much more chemically and mechanically robust than vacuum deposits and also are capable of transporting much more electrical current. ABS (acrylonitrile-butadiene-styrene) plastic dominates as the substrate of choice for most plated plastic applications because of a blend of mechanical and process properties and ability to be uniformly etched. The overall plating process comprises many steps. First, the plastic substrate is chemically etched to microscopically roughen the surface. This is followed by depositing an initial metal layer by chemical reduction. This initial metal layer is normally copper or nickel of thickness typically one-half micrometer. The object is then electroplated with metals such as bright nickel and chromium to achieve the desired thickness and decorative effects. The process is very sensitive to processing variables used to fabricate the plastic substrate, limiting applications to carefully molded parts and designs. In addition, the many steps employing harsh chemicals make the process intrinsically costly and environmentally difficult. The

- sensitivity of ABS plastic to liquid hydrocarbons has prevented certain applications. Other applications have been thwarted by the difficulty in achieving direct selective deposition of metals onto a plastic substrate using the process. The "electroless" chemical deposition employed to achieve initial metal deposition tends to coat the entire substrate surface. One approach has been to employ two material moldings, with only one of the materials being specially formulated to be "receptive" to the chemical deposition. Despite extensive development efforts, these techniques remain expensive, difficult, and unpredictable. Another approach has been to chemically deposit the initial metal over the entire substrate and then use combined photoetching and electroplating techniques to achieve a selective metal deposit. These processes are expensive, environmentally difficult and often impractical when considering complex three dimensional substrates. The conventional technology for electroplating on plastic (etching, chemical reduction, electroplating) has been extensively documented and discussed in the public and commercial literature. See, for example, Saubestre, Transactions of the Institute of Metal Finishing, 1969, Vol. 47., or Arcilesi et al., Products Finishing, March 1984.

- Many attempts have been made to simplify the process of electroplating on plastic substrates. Some involve special chemical techniques to produce an electrically conductive film on the surface. Typical examples of this approach are taught by U.S. Patent No. 3,523,875 to Minklei, U.S. Patent No. 3,682,786 to Brown et. al., and U.S. Patent No. 3,619,382 to Lupinski. The electrically conductive film produced was then electroplated.

- Another approach proposed to simplify electroplating of plastic substrates is incorporation of electrically conductive fillers into the resin to produce an electrically conductive plastic. The electrically conductive resin is then electroplated. Most of these attempts have failed. First, achieving electrodeposit adhesion to the filled polymer substrate has been problematic. Second, the conductivity

characteristic of filled resins is orders of magnitude less than typical metals.

Therefore, the electrodeposit does not coat the conductive resin instantly like a metal, but rather by lateral growth of electrodeposit over the surface. In most cases, these lateral growth rates are excessively slow. In some cases,

5. the conductive resin systems tend to form a non-conductive "skin" during processing and electrodeposition will not occur at all. Attempts to improve conductivity by increasing filler loading adversely affects costs, processability and mechanical properties. An example of this approach are the teachings of Adelman in U.S. Patent No. 4,038,042. Adelman taught incorporation of conductive
10. carbon black into a polymeric matrix to achieve electrical conductivity required for electroplating. The substrate was pre-etched to achieve adhesion of the subsequently electrodeposited metal. The technique suffered from excessively slow electrodeposit coverage rates.

- Luch however, in patent in 3,865,699 took a different approach. Luch taught
15. incorporation of small amounts of sulfur into polymer-based compounds filled with conductive carbon black. The sulfur was shown to have two advantages. First, it participated in formation of a chemical bond between the polymer-based substrate and an initial Group VIII based metal electrodeposit. Second, the sulfur greatly increased lateral growth of the Group VIII based metal electrodeposit over the
20. surface of the substrate, thereby avoiding a fatal problem with prior attempts to achieve direct electrodeposition on electrically conductive filled resins.

- Since the polymer-based compositions taught by Luch could be electroplated directly without any pretreatments, they could be accurately defined as directly electroplateable resins (DER). Directly electroplateable resins, (DER), are
25. characterized by the following features.

(a) having a polymer matrix;

- (b) presence of carbon black in amounts sufficient for the overall composition to have an electrical volume resistivity of less than 1000 ohm-cm., e.g., 100 ohm-cm., 10 ohm-cm., 1 ohm-cm.;
- (c) presence of sulfur (including any sulfur provided by sulfur donors) in amounts greater than about 0.1% by weight of the overall polymer-carbon-sulfur composition; and
- (d) presence of the polymer, carbon and sulfur in said directly electroplateable composition of matter in cooperative amounts required to achieve direct, uniform, rapid and adherent coverage of said composition of matter with an electrodeposited Group VIII metal or Group VIII metal-based alloy.
5. 10.

The minimum workable level of carbon black required to achieve electrical resistivities less than 1000 ohm-cm. appears to be about 8 weight percent based on the weight of polymer plus carbon black.

- There are a number of polymers suitable for the DER matrix. These include but are not limited to polyvinyls, polyolefins, polystyrenes, elastomers, polyamides, and polyesters, the choice generally being dictated by the physical properties required.
- 15.

In order to eliminate ambiguity in terminology of the present specification and claims, the following definitions are supplied.

20. "Metal-based" refers to a material having metallic properties comprising one or more elements, at least one of which is an elemental metal.

"Metal-based alloy" refers to a substance having metallic properties and being composed of two or more elements of which at least one is an elemental metal.

- "Polymer-based" refers to a substance composed, by volume, of 50 percent or more hydrocarbon polymer.
- 25.

"Group VIII-based" refers to a metal (including alloys) containing, by weight, 50% to 100% metal from Group VIII of the Periodic Table of Elements.

It is important to note that electrical conductivity alone is insufficient to permit a

plastic substrate to be directly electroplated. The plastic surface must be electrically conductive on a microscopic scale. For example, simply loading a small volume percentage of metal fibers may impart conductivity on a scale suitable for electromagnetic radiation shielding, but the fiber separation would likely prevent uniform direct electroplating. In addition, many conductive thermoplastic materials form a non-conductive surface skin during fabrication, effectively eliminating the surface conductivity required for direct electroplating.

OBJECTS OF THE INVENTION

An object of the invention is to eliminate the deficiencies in the prior art methods of producing expansive area, series interconnected photovoltaic arrays. A further object of the present invention is to provide improved substrates to achieve series interconnections among expansive thin film cells. A further object of the invention is to permit inexpensive production of high efficiency, heat treated thin film photovoltaic cells while simultaneously permitting the use of polymer based substrate materials and associated processing to effectively interconnect those cells. A further object of the present invention is to provide improved processes whereby expansive area, series interconnected photovoltaic arrays can be economically mass produced.

Other objects and advantages will become apparent in light of the following description taken in conjunction with the drawings and embodiments.

SUMMARY OF THE INVENTION

The current invention provides a solution to the stated need by producing the active photovoltaic film and interconnecting substrate separately and subsequently combining them to produce the desired expansive series interconnected array or module. The invention contemplates deposition of thin film photovoltaic junctions on metal foil substrates which can be heat treated following deposition in a continuous fashion without deterioration of the metal support structure. In a separate operation, an interconnection substrate structure is produced in a continuous roll-to-roll fashion.

The metal foil supported photovoltaic junction is then laminated to the interconnecting substrate structure and conductive connections are deposited to complete the array or module. In this way the interconnection substrate structure can be uniquely formulated from polymer-based materials since it does not have to endure high temperature exposure. Furthermore, the photovoltaic junction and its metal foil support can be produced in bulk without the need to use the expensive and intricate material removal operations currently taught in the art to achieve series interconnections. Finally, the process of the current invention is substantially fully additive, eliminating the wasteful and difficult scribing and material removal aspects of prior art teachings.

BRIEF DESCRIPTION OF THE DRAWINGS

The various factors and details of the structures and manufacturing methods of the present invention are hereinafter more fully set forth with reference to the accompanying drawings wherein:

Figure 1 is a top plan view of a thin film photovoltaic cell including its support foil.

Figure 2 is a sectional view taken substantially along the line 2-2 of Figure 1.

Figure 3 is an expanded sectional view showing a form of the structure of layer 11 of Figure 2.

Figure 4 illustrates a process for producing the structure shown in Figures 1 through 3.

Figure 5 is a sectional view illustrating the problems associated with making series connections among thin film photovoltaic cells shown in Figures 1-3.

Figure 6 is a top plan view of a substrate structure for achieving series interconnections of thin film photovoltaic cells.

Figure 7 is a sectional view taken substantially along the line 7-7 of Figure 6:

Figure 7A is a sectional view similar to Figure 7 of an alternate embodiment.

Figure 7B is a sectional view similar to Figure 7 and 7A of an additional

embodiment of a substrate structure.

Figure 8 is a sectional view similar to Figure 7 showing an alternate embodiment of a substrate structure for achieving series interconnections of thin film photovoltaic cells.

5. Figure 9 is a top plan view of an alternate embodiment of a substrate structure for achieving series interconnections of thin film photovoltaic cells.

Figure 10 is a sectional view similar to Figures 7 and 8 taken substantially along line 10-10 of Figure 9.

10. Figure 11 is a top plan view of another embodiment of a substrate structure for achieving series interconnections of thin film photovoltaic cells.

Figure 12 is a sectional view taken substantially along the line 12-12 of Figure 11.

15. Figures 13A and 13B schematically depict a process for laminating the foil supported thin film photovoltaic structure of Figures 1 through 3 to an interconnecting substrate structure. Figure 13A is a side view of the process. Figure 13B is a sectional view taken substantially along line 13b-13b of figure 13A.

Figures 14A, 14B, and 14C are views of the structures resulting from the laminating process of Figures 13 and using the substrate structure of Figures 7, 8 and 10 respectively.

20. Figures 15A, 15B, and 15C are sectional views taken substantially along the lines 15a-15a, 15b-15b, and 15c-15c of Figures 14A, 14B, and 14C respectively.

Figures 15D, 15E, and 15F are sectional views similar to Figures 15A - 15C illustrating additional embodiments of structures resulting from the laminating process of Figures 13A and 13B employing the substrate structures of Figures 7A and 7B.

25.

Figure 16 is a top plan view of the structure resulting from the laminating process of Figure 13 and using the substrate structure of Figures 11 and 12.

Figure 17 is a sectional view taken substantially along the line 17-17 of Figure 16.

Figure 18 is a top plan view of the structures of Figures 14A and 15A but following an additional step in manufacture of the interconnected cells.

5. Figure 19 is a sectional view taken substantially along the line 19-19 of Figure 18.

Figure 20 is a top plan view of a completed interconnected array.

Figure 21 is a sectional view taken substantially along line 21-21 of Figure 20.

10. Figure 22 is a sectional view similar to Figure 15A but showing an alternate method of accomplishing the mechanical and electrical joining of the lamination process of Figure 13.

Figure 23 is a sectional view similar to Figure 15A but showing an alternate embodiment of the laminated structure.

Figure 24 is a sectional view of an alternate embodiment.

15. Figure 25 is a sectional view of the embodiment of Figure 24 after a further processing step.

Figure 26 is a sectional view of another embodiment of a laminated intermediate article in the manufacture of series interconnected arrays.

20. Figure 27 is a top plan view of a starting material for another embodiment of substrate structure.

Figure 28 is a greatly magnified plan view of the material of Figure 27.

Figure 29 is a sectional view taken substantially along line 29-29 of Figure 28.

Figure 30 is a sectional view taken substantially along line 30-30 of Figure 28.

25. Figure 31 is a simplified sectional view representing the structure depicted in Figures 29 and 30.

Figure 32 is a view similar to Figure 27 but defining three distinct area portions of the structure produced by a process step.

Figure 33 is a greatly magnified plan view of that portion of Figure 32 defined by W2.

Figure 34 is a greatly magnified sectional view of a portion of the structure of Figure 33 taken substantially from the perspective of line 34-34 of Figure 33.

5. Figure 35 is a sectional view similar to Figure 34 showing the structure following an optional additional process step.

Figure 36 is a simplified plan view of the structure of Figure 32 useful in illustrating the process and structure of the embodiment.

10. Figure 37A is a simplified sectional view taken substantially along line 37-37 of Figure 36, useful in illustrating the process and structure of the embodiment.

Figure 37B is a simplified sectional view similar to Figure 37A incorporating an optional additional process step.

Figure 37C is a plan view similar to Figure 36 of an additional embodiment.

15. Figure 37D is a sectional view taken substantially along the line 37D-37D of Figure 37C.

Figure 38 is a schematic depiction of a process for joining the foil supported thin film photovoltaic structure of Figures 1 through 3 to the substrate structure of Figures 32 or 36.

Figure 39 illustrates one form of the process depicted in Figure 38.

20. Figure 40 is a view of the process of Figure 39 taken substantially along line 40-40 of Figure 39.

Figure 41 is a plan view of the structure resulting from the process of Figure 38.

Figure 42A is an embodiment of the structure of Figure 41 taken substantially along line 42-42 of Figure 41.

25. Figures 42B and 42C are views similar to 42A showing alternate embodiments of the structure depicted in Figure 41.

Figure 42D is a sectional view similar to Figures 42A - 42C of an additional embodiment.

Figure 43 is an enlarged view of the portion of Figure 42A shown within circle "A".

Figure 44 is a view similar to Figure 43 after a further processing step.

Figure 45 is a view similar to Figure 44 after a further processing step.

5. Figure 46 is a top plan view of another embodiment of the novel substrate structures useful in the manufacture of series interconnected photovoltaic arrays.

Figure 47 is a sectional view taken substantially along line 47-47 of Figure 46.

Figure 48 is a view similar to Figure 47 following an additional processing step.

Figure 49 is a view similar to Figure 43, illustrating a different processing path.

10. Figure 50 is a plan view of an additional electrical circuit application made possible with directly electroplateable resins.

Figure 51 is a sectional view taken substantially along the line 51-51 of Figure 50.

15. Figure 52 is a view similar to 51 of the article of Figures 50 and 51 after an additional processing step.

Figure 53 is a plan view of an additional three dimensional electrical circuitry application made possible with directly electroplateable resins.

Figure 54 is a sectional view taken substantially along the line 54-54 of Figure 53.

20. Figure 55 is a sectional view of the article of Figures 53 and 54 following an additional processing step.

DESCRIPTION OF PREFERRED EMBODIMENTS

Reference will now be made in detail to the preferred embodiments of the invention, examples of which are illustrated in the accompanying drawings. In the drawings, like reference numerals designate identical or corresponding parts

25. throughout several views and an additional letter designation is characteristic of a particular embodiment.

Referring to Figures 1 and 2, a thin film photovoltaic cell, including a supporting

conductive foil is generally indicated by 10. Cell 10 has a top surface 59 and comprises a thin film photovoltaic structure 11 supported by conductive foil 12.

Foil 12 has a width X-10, length Y-10 and thickness Z-10. The full extent of width X-10 of foil 12 defines a first photovoltaic cell terminal edge 45 and second

5. photovoltaic cell terminal edge 46. It is contemplated that length Y-10 is considerably greater than width X-10 and length Y-10 can generally be described as "continuous" or being able to be processed in a roll-to-roll fashion. Foil 12 has first surface 65 and second surface 66. Typically foil 12 will be metal-based. A metal-based foil 12 may be of uniform composition or may comprise a laminate of
10. two or more metal-based layers. For example, foil 12 may comprise a base layer of inexpensive and processable metal 13 with an additional metal-based layer 14 disposed between base layer 13 and photovoltaic structure 11. The additional metal-based layer may be chosen to ensure good ohmic contact between the top surface 65 of support 12 and photovoltaic structure 11. Bottom surface 66 of foil
15. support 12 may comprise a material 75 chosen to achieve good electrical and mechanical joining characteristics to the substrate as will be shown. The thickness Z of support layer 12 is generally contemplated to be between .001 cm. and .025 cm. This thickness would provide adequate handling strength while still allowing flexibility for roll-to-roll processing. Those skilled in the art will understand that
20. cell 10 is substantially devoid of holes extending from top surface 59 to bottom surface 66 which would result in discontinuities in photovoltaic structure 11 and could adversely affect cell performance through electrical shorting.

Photovoltaic structure 11 can be any of the thin film structures known in the art.

- In its simplest form, the photovoltaic cell combines an n-type semiconductor with a
25. p-type semiconductor to form an n-p junction. Figure 3 illustrates an example of a typical photovoltaic cell structure in section. In Figure 3 and many subsequent figures the arrow labeled "hv" is used to indicate the light incident side of the

structure. In Figure 3, 15 represents a thin film of a p-type semiconductor, 16 a thin film of n-type semiconductor and 17 the resulting photovoltaic junction. The exact nature of the photovoltaic structure 11 does not form the subject matter of the present invention. Most often an optically transparent window electrode 18 such as

5. a thin film of zinc or tin oxide is positioned over photovoltaic structure 11 to minimize resistive losses involved in current collection. Photovoltaic structure 11 and window electrode 18 cover substantially the entire top surface 65 of foil support 12.

Figure 4 refers to the method of manufacture of the foil supported photovoltaic structures generally illustrated in Figures 1 through 3. The metal-based support foil

10. 12 is moved in the direction of its length Y through a deposition process, generally indicated as 19. Process 19 accomplishes deposition of the active photovoltaic structure onto support foil 12. Support foil 12 is unwound from supply roll 20a, passed through deposition process 19 and rewound onto takeup roll 20b. Process 19 can comprise any of the processes well-known in the art for depositing thin film

15. photovoltaic structures. These processes include electroplating, vacuum sputtering, and chemical deposition. Process 19 may also include treatments, such as heat treatments, intended to enhance photovoltaic cell performance.

Referring now to Figure 5, there are illustrated cells 10 as shown in Figure 2. The cells have been positioned to achieve spacial positioning on the support

20. substrate 21. Support structure 21 is by necessity non-conductive at least in that distance indicated by numeral 70 separating the adjacent cells 10. This insulating space prevents short circuiting from metal foil electrode 12 of one cell to foil electrode 12 of an adjacent cell. In order to achieve series connection, electrical communication must be made from the top surface of window electrode 18 to the foil

25. electrode 12 of an adjacent cell. This communication results in a net current flow in the direction of the arrow identified as "i" in figure 5. This net current flow direction is substantially parallel to width X-10 of foil 12 of photovoltaic cell 10. Electrical communication from top surface of window electrode 18

to the foil electrode 12 of an adjacent cell is shown in Figure 5 as a metal wire

41. Metal wire 41 is clearly impractical for inexpensive continuous production

and is shown for illustration purposes only. It should be noted that foil electrode 12

is relatively thin, on the order of .001 cm to .025 cm. Therefore connecting to its

5. edge as indicated in Figure 5 would be impractical.

Referring now to Figures 6 and 7, one embodiment of the interconnecting
and supporting substrate structures of the current invention is generally indicated by

22. Unit of substrate 22 comprises electrically conductive resin sheet 23 and

electrically insulating joining portion region 25. Region 25 has non-conductive top

10. and bottom surfaces 8 and 9 respectively. Electrically conductive region 23 has a

top, light incident side top conductive surface 26 extending between a

first terminal edge 29 and a second terminal edge 30 of conductive surface 26.

Width X-23 defines first and second terminal edges 29 and 30 respectively.

Electrically conductive region 23 also has a bottom surface 28, length Y-23 and

15. thickness Z-23. Top surface 26 of conductive sheet 23 can be thought of as having

top collector surface 47 and top contact surface 48 separated by imaginary

insulating boundary 49. The purpose for these definitions will become clear in the

following.

An alternate embodiment of unit of substrate, indicated as 23a, is

20. illustrated in Figure 7A. In the Figure 7A embodiment, the electrically

insulating joining portion 25 is positioned to overlay a portion of electrically

conductive sheet 23. In this case, width dimension X-23 extending between

terminal edges 29 and 30 of the conductive top surface 26 is less than the full

width of electrically conductive sheet 23.

25. A further embodiment of unit of substrate, indicated as 22b, is illustrated

in Figure 7B. In the Figure 7B embodiment, the electrically insulating joining

portion 25 is positioned on the lower surface 28 of electrically conductive

sheet 23.

Electrically conductive sheet 23 includes an electrically conductive polymer.

Typically, electrically conductive polymers exhibit bulk resistivity values of less than 1000 ohm-cm. Resistivities less than 1000 ohm-cm can be readily achieved by compounding well-known conductive fillers into a polymer matrix binder.

5. The substrate unit 22 may be fabricated in a number of different ways.

Electrically conductive sheet 23 can comprise an extruded film of electrically conductive polymer joined to a strip of compatible insulating polymer 25 at or near terminal edge 29 as illustrated in Figure 7. Alternatively, the conductive sheet may comprise a strip of electrically conductive polymer 23a laminated to an insulating support structure 31 as illustrated in section in Figure 8. In Figure 8, electrically insulating joining portions 25a are simply those portions of insulating support structure 31 not overlaid by sheets 23a.

10. It is contemplated that electrically conductive sheets 23 may comprise materials in addition to the electrically conductive polymer. For example, a metal may be electrodeposited to the electrically conductive polymer for increased conductivity. In this regard, the use of a directly electroplateable resin (DER) may be particularly advantageous. DER's cover with metal rapidly by lateral growth of electrodeposit. In addition, selective metal coverage of a multi material structure is readily achieved when one of the materials is DER. For example, were region 23 shown in Figure 7 to comprise a DER, exposing the Figure 7 structure to a Group VIII electroplating bath would result in rapid metal electrodeposition of region 23 only while region 25 would remain unplated. The fact that DER's are readily fabricated either as bulk compositions or coatings qualifies DER's as being eminently suitable in this application.

25. A further embodiment of fabrication of substrate unit 22 is illustrated in Figures 9 and 10. In Figure 9, electrically conductive sheet 23b comprises electrically

conductive polymer impregnated into a fabric or web 32. A number of known techniques can be used to achieve such impregnation. Insulating joining portion 25b in Figure 9 is simply an un-impregnated extension of the web 32. Fabric or web 32 can be selected from a number of woven or non-woven fabrics, including non-

5. polymeric materials such as fiberglass.

Referring now to Figure 11, an alternate embodiment for the substrate structures of the present invention is illustrated. In the Figure 11, a support web or film 33 extends among and supports multiple individual cell units, generally designated by repeat structure 34. Electrically conductive sheets 35 are analogous to sheet 23 of

10. Figures 6 through 10. At the stage of overall manufacture illustrated in Figure 11, electrically conductive sheets 35 need not comprise an electrically conductive polymer as do sheets 23 of Figures 6 through 10. However, as will be shown, electrically conducting means, typically in the form of an electrically conductive polymer containing adhesive, must eventually be utilized to join photovoltaic

15. laminate 10 to the top surface 50 of electrically conductive sheets 35. In addition, the electrically conducting sheets 35 must be attached to the support carrier 33 with integrity required to maintain positioning and dimensional control. This is normally accomplished with an adhesive, indicated by layer 36 of Figure 12.

Conductive sheets 35 are shown in Figures 11 and 12 as having length Y-35, width X-35 and thickness Z-35. It is contemplated that length Y-35 is considerably

20. greater than width X-35 and length Y-35 can generally be described as "continuous" or being able to be processed in roll-to-roll fashion. Width X-35 defines a first terminal edge 53 and second terminal edge 54 of sheet 35.

It is important to note that the thickness of the conductive sheets 35, Z-35 must

25. be sufficient to allow for continuous lamination to the support web 33. Typically when using metal based foils for sheets 35, thickness between .001 cm and .025 cm would be chosen.

As with the substrate structures of Figures 6 through 10, it is helpful to characterize top surface 50 of conductive sheets 35 as having a top collector surface 51 and a top contact surface 52 separated by an imaginary barrier 49. Conductive sheet 35 also is characterized as having a bottom surface 80.

5. It is readily realized that the photovoltaic cells 10 of Figures 1-3 are separately prepared and are initially separate and distinct from the substrate support structures taught in Figures 6-12. This initial separation permits elevated temperature processing of cells 10 while permitting design of unique polymer containing interconnect substrate structures. The polymer containing substrates can be made flexible and be processed in continuous roll to roll fashion.

10. Referring now to Figures 13A and 13B, a process is shown for laminating the metal-based foil supported thin film photovoltaic structure of Figures 1 through 3 to the substrate structures taught in Figures 6 through 12. In figures 13A and 13B, photovoltaic cell structures as illustrated in Figures 1 through 3 are indicated by numeral 10. Substrate structures as taught in the Figures 6 through 12 are indicated by the numeral 22. Numeral 42 indicates a film of electrically conductive adhesive intended to join electrically conductive metal-based foil 12 of Figures 1 through 3 to electrically conductive sheet 23 of Figures 6 through 10 or electrically conductive sheets 35 of Figures 11 and 12. It will be appreciated by those skilled in the art that the adhesive strip 42 shown in Figures 13A and 13B is one of but a number of appropriate metal joining techniques which would maintain required ohmic communication. For example, it is contemplated that methods such as doctor blading a conductive resin prior to lamination, spot welding, soldering, joining with low melt temperature alloys, or crimped mechanical contacts would serve as equivalent methods to accomplish the ohmic joining illustrated as achieved in Figures 13A and 13B with a strip of conductive adhesive. These equivalent methods can be generically referred to as conductive joining means. In Figure 13B, the process of Figure 13A is illustrated using the substrate of Figures 6 and 7.

Referring now to Figures 14 and 15, there is shown the result of the lamination process of Figure 13A and 13B using the substrate structure of Figures 6 through 10.

Figures 14A and 15A correspond to the substrate structures of Figures 6 and 7.

Figures 14B and 15B correspond to the substrate structure of Figure 8. Figures 14C

5. and 15C correspond to the substrate structures of Figures 9 and 10. Figure 15D corresponds to the substrate structure of Figure 7A. Figures 15E and 15F correspond to the substrate structure of Figure 7B.

Figures 14A through 14C show that the cells 10 have been positioned with first terminal edge 45 of one cell being substantially parallel to second

10. terminal edge 46 of an adjacent cell.

In the Figures 15A, 15B and 15C, electrically conductive adhesive layer 42 is shown as extending completely and contacting the entirety of the second surface 66 of metal-based foil supported photovoltaic cells 10. This complete surface coverage is not a requirement however, in that foil 12 is highly conductive and able to

15. distribute current over the expansive width X-10 with minimal resistance losses. For example, the structure of Figure 22 shows an embodiment wherein electrical communication is achieved between conductive sheet 23 of Figures 6 and 7 and second surface 66 of foil 12 through a narrow bead of conductive joining means 61. An additional bead of adhesive shown in Figure 22 by 44, may be used to ensure
20. spacial positioning and dimensional support for this form of structure. Adhesive 44 need not be electrically conductive.

In the Figures 15D and 15E discrete units of substrate structure are joined through cells 10. In Figure 15F multiple units of substrate structure are joined in an integral fashion.

25. It is important to note that in the embodiments of Figures 15A, 15C, 15D, 15E, and 15F, the bottom surface 28 of conductive region 23 remains exposed. Those skilled in the art will recognize the advantages of the conductive bottom surface should electroplating of final interconnection

(for example, grid fingers) be considered.

In the Figures 15A, 15B and 15C, the conductive sheets 23, 23a and 23b are shown to be slightly greater in width X-23 than the width of foil X-10. As is shown in Figure 23, this is not a requirement for satisfactory completion of the series

5. connected arrays. Figure 23 is a sectional view of a form of the substrate structures of Figures 6 and 7 laminated by the process of Figure 13 to the photovoltaic structures of Figures 1-3. In Figure 23, width X-10 is greater than width X-23.

Electrical communication is achieved through conductive joining means 42 and additional joining means 44 to achieve dimensional stability may be employed. The

10. only requirement of the current invention is that first conductive sheet terminal edge 29 be offset from first photovoltaic cell terminal edge 45 to expose a portion of top surface 26 of conductive sheet 23.

In Figure 23, insulating joining portion 25 is shown as extending continuously from second terminal edge 30 of one conductive sheet 23 to the first terminal edge

15. 29 of an adjacent conductive sheet. As shown in Figure 26, this is not necessary. In Figure 26, metal foil supported photovoltaic cell 10 is attached to a first conductive sheet 23 through electrically conductive joining means 42 and also to insulating joining portion 25 of an adjacent substrate structure through adhesive 44. Thus, the substrate structure 22 can be discrete. In the embodiment of Figure 26, the foil
20. based photovoltaic structure 10 is of sufficient strength to maintain proper spaced relationships and positioning among cells.

Referring now to Figures 16 and 17, there is shown an alternate structure resulting from the laminating process of Figure 13 as applied to the photovoltaic cells of Figures 1-3 and the substrate structure of Figures 11 and 12. In a fashion
25. similar to that of Figures 15, 22, and 23, the first terminal edge 53 of conductive sheets 35 supported by insulating substrate 33 are slightly offset from the first terminal edge 45 of photovoltaic cells 10. This offset exposes a portion of top

surface 50 of conductive sheet 35. Electrical and mechanical joining of sheets 35 with second surface 66 of metal-based foil 12 is shown in Figure 17 as being achieved with conductive adhesive 42 as in previous embodiments. However, it is contemplated as in previous embodiments that this electrical and mechanical joining can be accomplished by alternate means such as soldering, joining with compatible low melting point alloys, spot welding, or mechanical crimping.

In the figure 17, support structure 33 is shown as extending continuously among many cells. However, it should be clear that support film 33 can be discontinuous. Support film 33 need only be attached to a portion of a first sheet 35 associated with a first cell and a portion of the bottom surface 66 of an adjacent cell. This arrangement would suffice to achieve the desired spacial positioning among cells and leave exposed a portion of back surface 80 of electrically conductive sheet 35.

Referring to Figures 14 through 17, those skilled in the art will recognize that the process of Figure 13A and 13B results in a substantially planar sheet comprising photovoltaic cells positioned on a substrate having selected conductive and non-conductive regions. Further comparing the sectional views of Figures 15, 22, 23 and 17, one observes many similarities. The most important common structural similarity is that the first terminal edges 29 of conductive sheets 23 be offset slightly from first terminal edge 45 of photovoltaic cells 10 (figures 15, 22, 23). Similarly, first terminal edges 53 of conductive sheets 35 are slightly offset from first terminal edges 45 of photovoltaic cells 10 (Figure 17). As will be shown, the resulting exposed top surface portions are used as contact surfaces for the final interconnected array.

It should also be observed that the structures equivalent to those shown in Figures 16 and 17 can also be achieved by first joining photovoltaic cells 10 and conductive sheets 35 with suitable electrically conductive joining means 42 to give the structure shown in Figure 24 and laminating these strips to an insulating support

web 33. An example of such an equivalent structure is shown in Figure 25, wherein the laminates of Figure 24 have been adhered to insulating web 33 in defined repeat positions with adhesive means 57 and 44. As mentioned above and as shown in Figures 24 and 25, conductive sheets 35 do not have to contact the whole of the bottom surface 66 of photovoltaic cell 10. In addition, support web 33 need not be continuous among all the cells. The support web 33 need only extend from the adhesive means 57 of one cell to the adhesive attachment 44 of an adjacent cell. This arrangement would leave a portion of the bottom surface 66 of foil 12, and perhaps a portion of the bottom surface 80 of conductive sheet 35 exposed.

5. Referring now to Figures 18 and 19, insulating beads 56 and 60 of insulating material having been applied to the first and second terminal edges 45 and 46 respectively of photovoltaic cells 10. While these beads 56 and 60 are shown as applied to the structure of Figure 15a, it is understood that appropriate beads of insulating material are also envisioned as a subsequent manufacturing step for the structures of 15b, 15c, 17, 22, 23, 25, and 26. The purpose of the insulating beads is to protect the edge of the photovoltaic cells from environmental and electrical deterioration. In addition, as will be shown the insulating bead allows for electrical interconnections to be made among adjacent cells without electrical shorting.

10. It is noted that the application of insulating material 56 to first terminal edge 45 of photovoltaic cells 10 effectively divides the top surfaces 26 and 50 of conductive sheets 23 and 35 respectively into two regions. The first region (region 48 of surface 26 or region 52 of surface 50) can be considered as a contact region for series interconnects among adjacent cells. The second region (region 47 of surface 26 or region 51 of surface 50) can be considered as the contact region for interconnecting the substrate to the second surface 66 of photovoltaic cells 10.

20. Referring now to Figures 20 and 21, there is shown the method of forming the final interconnected array. Grid fingers 58 of a highly electrically conductive material are deposited to achieve electrical communication between the top surface

59 of the photovoltaic cell 10 and the remaining exposed contact regions 48 or 52 of an adjacent cell. It is contemplated that these fingers can be deposited by any of a number of processes to deposit metal containing or metal-based foils or films, including masked vacuum deposition, printing of conductive inks, electrodeposition or combinations thereof. The grid fingers are not considered to be a part of the substrate structure since they do not contribute to supporting and spacial positioning of the cells. They serve only to achieve final electrical interconnection. In Figures 20 and 21, the direction of net current flow through the interconnected array illustrated is indicated by the arrow identified as "i", which direction is substantially parallel to width X-23 of electrically conductive region 23 of unit of substrate 22.

Referring now to Figure 27, the starting material for yet another embodiment is illustrated in plan view. Material 90 comprises a web, mesh or fabric and is characterized by having a width "W" and length "L". It is contemplated that length "L" is considerably greater than width "W" and length "L" can generally be described as "continuous" or being able to be processed in a roll-to-roll fashion.

Figure 28, a greatly magnified plan view of a portion of the structure of Figure 27, shows the material 90 comprising fibrils 92 interwoven to form a sturdy structure. Holes 94 are present among the interwoven fibrils. It is understood that the fibrils need not be actually interwoven as shown. Equivalent structures comprising fibrils and holes, such as polymeric non-woven fabric or adhesively bonded fibril mats, can be employed.

Figures 29 and 30 are sectional views of the embodiment of Figure 28 taken substantially along line 29-29 and line 30-30 of Figure 28 respectively.

Figure 31 is a greatly simplified sectional representation of the structure depicted in Figures 29 and 30. This simplified representation of Figure 31 is useful in the illustration of subsequent embodiments.

- Referring now to Figure 32, there is shown the material shown in Figure 27 following an additional processing step. The material of width "W" is now generally designated as 104 to indicate this additional process step. Width "W" has been further defined as comprising three minor widths "W1", "W2", and "W3". Each of
5. these widths "W1", "W2", and "W3" is understood to extend along length "L" as indicated. In light of the following description and embodiments, it will be understood by those skilled in the art that regions "W1" and "W3" need not contain holes 94 but may be chosen to be a solid sheetlike film. Furthermore, region "W3" may be eliminated entirely in some embodiments.
10. Figure 33 is a greatly magnified plan view of the portion of Figure 32 structure identified as minor width "W2". In contrast to the plan view shown in Figure 28, the structure of Figure 33 appears continuous in the two-dimensional plan view. This continuity results from coating the fibrils with an electrically conductive coating. The structure of the coated fibrils is best shown in the sectional view of Figure 34, which
15. is a view taken substantially along line 34-34 of Figure 33. In Figure 34, fibrils 92 in the region "W2" have been coated with electrically conductive coating material 96. It is anticipated that coating 96 and the deposition process for applying coating 96 can be chosen from any number of suitable techniques. Included in such techniques are painting, dipping, or printing of conductive inks, laminating, and masked chemical or
20. vapor deposition of metals or other conductive materials. In the case of a temperature resistant fabric such as fiberglass, deposition of a low melting point metal such as solder could be employed. A particularly advantageous coating 96 to prepare the structure of Figure 34 is DER applied using a paint solution or paste. The DER is inexpensive, and readily formulated and applied from solution form.
25. The important feature of the structure of Figure 34 is that through-hole electrical communication extends from the top surface 98 to the bottom surface 100 in the region defined by "W2" of Figure 34. This situation is readily achieved by using the coated fabric approach of the present embodiments.

Figure 35 is a sectional view similar to Figure 34 following an additional optional process step. In Figure 35, the electrical conductivity and mechanical and environmental integrity of the structure is further enhanced by applying an additional highly conductive coating 102 overlaying coating 96. This subsequent coating 102 can be conveniently applied by metal electrodeposition. The structure of Figure 35 gives highly conductive communication equivalent to a metal screen from top surface 98 to bottom surface 100 in region "W2" by virtue of the through-hole electrodeposition.

Referring now to Figures 36, there is shown a simplified plan view of the 104 structure intended to facilitate teaching of the processing steps envisioned to accomplish manufacture of the series connected photovoltaic arrays using the substrate structure 104. In Figure 36, the regions "W1" and "W3" have structure shown in detail in Figures 28-30. In Figure 36, region "W2" has structure shown in detail in Figures 33 and 34 and optionally Figure 35.

Referring now to Figure 37A, there is shown a simplified sectional view of the 104 structure employing the "W2" structure depicted in Figure 34. Figure 37B shows a similar view of the 104 structure employing the "W2" structure depicted in Figure 35. These simplifications will help illustration of the processing steps and the structures resulting from these processing steps.

Figures 37C and 37D show an alternate embodiment. In the Figures 37C and 37D numeral 230 indicates a metal mesh or screen whereas 232 and 234 represent a polymer based film or web joined to metal screen 230. This is an equivalent alternate structure to that of 37A and 37B.

Referring now to Figure 38 there is shown a schematic depiction of a process for joining the foil supported thin film photovoltaic structure of Figures 1 through 3 with the substrate strips 104. Photovoltaic cells 10 are continuously fed to the process in spaced relationship to substrate strips 104. The process accomplishes attaching one

edge portion of cells 10 to a portion "W3" of one substrate strip 104 and an opposite edge portion of cells 10 to a portion "W1" of a second substrate strip 104.

Figures 39 and 40 illustrate the process of Figure 38 in more detail. In Figure 39, spacially positioned substrate strips 104 are continuously fed to the joining process 110 from roll 106. Spacially positioned photovoltaic cells 10 are continuously fed to the process 110 from roll 108. The resultant combined structure is designated by the numeral 112.

Figure 40 illustrates the process of Figure 39 from the perspective of line 40-40 of Figure 39.

Figure 41 is a plan view of the combined structure resulting from joining process 110.

Figure 42A is a simplified sectional view taken substantially along line 42-42 of Figure 41 of the product from process 110 when structure 104 shown in Figure 37A is employed. Adhesive bead 114 is used to attach a first edge portion 118 of photovoltaic cell structure 10 to portion "W3" of a substrate strip and adhesive bead 116 attaches the second edge portion of cell 10 to portion "W1" of another substrate strip. Insulating beads 56 and 60 protect the first and second terminal edges of photovoltaic cells 10.

Figure 42B is a structure similar to 42A but shows that the substrate structure need not be discrete strips but can be joined. This is equivalent to stating the portion "W1" of one strip is joined to portion "W3" of another strip. Maintenance of spacial positioning and mechanical integrity are promoted by the structure depicted in Figure 42B.

Figure 42C is a view similar to 42a but employing the substrate structure 104 shown in Figure 37B.

Figure 42D shows that portion "W3" of the prior embodiments may be eliminated. In this case terminal edge 118 of cell 10 is attached via

adhesive bead 115 to upper surface 98 of portion "W2". It is advantageous to choose a conductive adhesive for bead 115 of Figure 42D since this facilitates final interconnection of the array or module.

Figure 43 is an enlarged view of the structural portion within circle "A" of Figure 42A.

Referring to Figures 42 A-D and 43, those skilled in the art will recognize that the process of Figure 38 results in a substantially planar sheet comprising photovoltaic cells positioned on a substrate having selected conductive and non-conductive regions. The cells are positioned such that a gap 160 is created between adjacent cells. Gap 160 extends at least partially over conductive regions W-2 and portions of non-conductive regions W-1, W-2. Holes extend from the top surface 98 to the bottom surface 100 at least in the region comprising conductive material 96.

Figure 44 is a view similar to Figure 43 but following an additional manufacturing step in preparation of the series connected array. In Figure 44 an electrically conductive coating 122 extends from the top surface 59 of photovoltaic cell 10a over insulating bead 60 and to electrically conductive region "W2". Coating 122 can comprise a number of electrically conductive media, such as conductive inks. Appropriate conductive inks can be applied by silk screening, printing, or simple masked printing. Alternate forms of applying coating 122 are chemical or vacuum deposition of conductive materials in conjunction with appropriate masking techniques.

As indicated in Figure 44, conductive coating 122 extends outward across the surface of cell 10 in the form of a grid finger. These grid fingers obviously do not cover the entire top surface 59 of cell 10, but are positioned in spaced relationship on the surface as illustrated previously in Figure 20.

Figure 44 also shows an electrically conductive coating 124 extending from the second lower surface 66 of cell 10b and to electrically conductive region "W2".

Coating 124 need not be the same composition nor applied by the same process as coating 122.

As shown in Figure 44, electrical communication is established between the top surface 59 of photovoltaic cell 10a and the bottom surface 66 of adjacent

5. photovoltaic cell 10b. However, most often the coatings 122 and 124 do not show optimal conductivity, either because coating resistivities are high relative to metals or coating thickness' are small. Therefore, Figure 45 shows the possibility of reducing resistive power losses by applying an additional coating of pure metal. This additional coating 126 is preferably applied by electrodeposition, although chemical and vapor deposition can be employed. Thus, the use of DER in coating forms would serve as an excellent choice for coatings 122 and 124. The fact that second surface 100 of conductive region "W2" remains exposed in this embodiment allows for simple electrical contact for electroplating, a situation analogous to having conductive region surface 28 exposed as pointed out
10. in prior embodiments. Obviously in all cases appropriate masking techniques, well know in the art, would be employed to prevent deposition on top surface 59 in those areas not covered with grid fingers. In the case of this final deposition of highly conductive metal, the through-hole conductivity characteristic of the substrate allows for a continuous highly conductive path from top surface 59 of one cell to bottom
15. surface 66 of an adjacent cell. As with the embodiments of Figures 5, 20 and 21, the net current flow among cells in the embodiments of Figures 44 and 45 will be understood by those skilled in the art to be in the direction of the arrow labeled "i" in Figures 44 and 45.
- 20.

Figure 49 is a sectional view similar to Figure 43 of an alternative

25. intermediate article resulting from feeding the material of Figures 27 through 31 to the process of Figures 38 through 40 rather than the joining strips 104 of Figures 36 and 37. Here the conductive coating 96 defining region "W2" of figures 36 and 37

has not been applied. However, applying the conductive coating 96 to the Figure 49 structure at the time of applying conductive coatings 122 and 124 (see discussion of Figure 44), results in converting the Figure 49 structure into one equivalent to that shown in Figure 44.

5. Figure 46 shows yet another embodiment of the current disclosure. The plan view of 46 illustrates a polymer based sheet 130 of width "W" subdivided into three areas "W1", "W2", and "W3" in fashion similar to that of Figure 32. Polymer based sheet 130 can be conveniently formed by coextrusion of materials 132, 134, and 136, corresponding to regions "W1", "W2", and "W3" respectively. Materials 132, 134, and 136 can be all based on the same polymer or different polymers can be chosen. It is important however that proper joining integrity be established at mating interfaces 138 and 140.
- 10.

- The material 134 chosen for region "W2" is an electrically conductive polymer and may be a woven or non-woven fabric of electrically conductive polymer. In addition, it may be advantageous to electroplate the fabric, in which case a fabric comprising DER would be a preferred choice.
- 15.

- Figure 47 is a sectional view taken substantially along line 47-47 of Figure 46. As shown in Figures 46 and 47, region "W2" is caused to have holes 142 along its length. In the simplest conceptual case, these holes are simply punched in the region "W2". Another approach would be to formulate the region "W2" of Figures 46 and 47 from a fabric (non-woven or woven) of electrically conductive polymer.
- 20.

- Figure 48 shows the structure of Figure 47 following an additional processing step of depositing metal 144 through holes 142 to establish high electrical conductivity from top surface 146 to bottom surface 148. Preferably this metal deposition is by electroplating although chemical and vapor deposition techniques could be used.
- 25.

In many respects the structures shown in Figures 47 and 48 resemble the structures depicted in Figures 37a and 37b respectively. Thus the use of the

structures of Figures 47 and 48 in the process of Figures 38 through 40 would give results similar to those previously taught as one skilled in the art will recognize.

It is important to point out that many of the unique substrate and collector grid structures taught in the foregoing text are in fact selectively electroplated

5. articles made possible by joining directly electroplateable resins (DER's) with other non-conductive materials in a multi-component article. This selectivity of DER's can be employed for significant advantages in both functional and decorative applications. For example, combining DER with a thermoplastic elastomer in a two shot molding is contemplated to combine a bright metal appearance with a soft grip for items such as knobs and handles.
- 10.

Another functional application using the selectivity of DER is illustrated in Figures 50 and 51. Figures 50 and 51 show a DER material 202 fabricated in a loop design into the surface of a flat non-conductive substrate 204.

- Additionally a mounting pad 206 is incorporated into the DER portion of the
15. two material article further incorporating wells 207. Subjecting the article illustrated in Figures 50 and 51 to an electroplating step results in the article illustrated in Figure 52. In Figure 52, electrodeposit 208 is deposited selectively on the surface of DER material 202, leaving the surface of 204 electrically insulating. It is also understood that wells 207 and pads 206 will
20. also cover with electrodeposit. The electrodeposit coating the surface of wells 207 allows excellent ohmic contact to the connector pins of a microchip, while the electrodeposit on the loop structure gives a robust and highly conductive antenna for transformer power transmission. These features are highly desirable in the production of "smart cards" (or chip cards) used for
25. convenient transportable information storage.

An additional application for the electrodeposition selectivity of DER materials is illustrated in Figures 53 through 55. These Figures illustrate a multicomponent article using DER to produce a conductive trace of a

geometrically complex three-dimensional molding. In Figures 53 through 55, DER material is identified as numeral 214, and non-conductive material as 212.

It is contemplated that the article illustrated in Figures 53 and 54 is produced by a multi-component (generally referred to as a multi color) molding process.

5. Figures 53 and 54 also illustrate a well 220 and through-hole 218 which are molded into the object. Figure 55 illustrates the article following exposure to an electroplating process. In Figure 55, numeral 216 illustrates an electrodeposit. Electrodeposit 216 is understood to be either a single layer or multiple layers of metal as is understood in the electroplating art. The
10. electrodeposit supplies a robust highly conductive circuit in a pattern difficult to achieve by alternate conventional techniques.

It is important to recognize that many known or useful circuits could be manufactured using the teachings described above. These include but are not limited to, many circuit boards, capacitors, induction heaters, connectors,

15. switches, resistors, inductors, batteries, fuel cells, coils, signal lines, power lines, radiation reflectors, and sensors.

- It is important to recognize that the unique design and process taught by the present disclosure is accomplished in a substantially fully additive fashion. No wasteful and costly material removal steps are needed to achieve the integrated
20. series connected modules or arrays and electrical circuitry taught. This is a significant advantage over the prior art.

- Although the present invention has been described in conjunction with preferred embodiments, it is to be understood that modifications, alternatives and equivalents may be included without departing from the spirit and scope of the inventions, as
25. those skilled in the art will readily understand. Such modifications, alternatives and equivalents are considered to be within the purview and scope of the invention and appended claims.

WHAT IS CLAIMED IS:

In combination, multiple series connected photovoltaic cells and a substrate structure,

- said photovoltaic cells including a support structure comprising a cell metal-based foil having a light incident top surface and a bottom surface separated by a thickness,
- said photovoltaic cells further comprising semiconductor material positioned on at least a portion of said light incident top surface,
- said cell metal-based foil having a foil length and foil width, the full extent of said foil width specifying a linear dimension whose endpoints define first and second cell foil terminal edges,
- said substrate structure comprising one or more electrically conductive substrate regions, said conductive substrate regions comprising a conductive surface extending over a conductive surface width, said conductive surface width defined as being in the direction of net current flow between series connected cells, the maximum extent of said conductive surface width defining first and second terminal edges of said conductive surface,
- said substrate structure further comprising at least one electrically non-conductive substrate region joined to a first of said conductive substrate regions,
- said combination characterized by having said bottom surface of said support structure of a first of said cells attached to said conductive surface of said first of said conductive regions and having said bottom surface of said support structure of a second of said cells being attached to said at least one non-conductive substrate region,
- said combination further characterized by having cell positioning of said cells on said substrate structure such that a line drawn in said conductive surface width direction between said terminal edges of said conductive surface is intersected by a said cell terminal edge of at most a single cell.

ABSTRACT OF THE DISCLOSURE

This invention comprises deposition of thin film photovoltaic junctions on conductive foil substrates which can be heat treated following deposition in a continuous fashion without deterioration of the metal support structure. In a separate operation, an interconnection substrate structure is produced in a continuous roll-to-roll fashion. The conductive foil supported photovoltaic junction is then laminated to the interconnection substrate structure and conductive connections are deposited to complete the module or array. In this way the interconnection substrate structure can be uniquely formulated from polymer-based materials since it does not have to endure high temperature exposure. Furthermore, the photovoltaic junction and its conductive foil support can be produced in bulk without the need to use the expensive and intricate material removal operations currently taught in the art to achieve series interconnections.

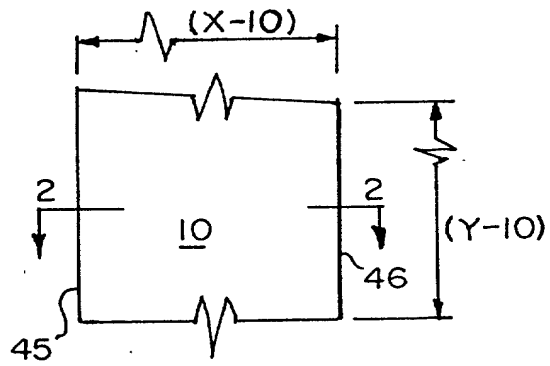


FIG. 1

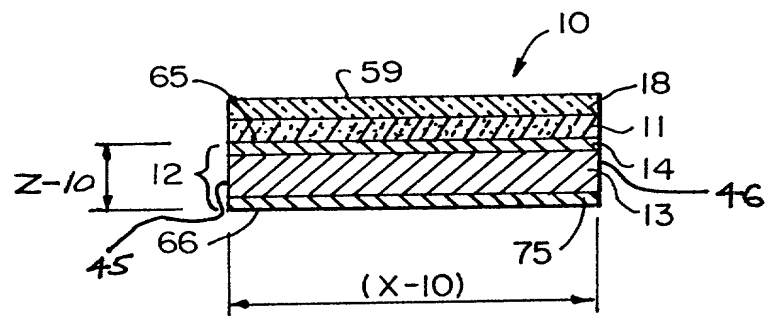


FIG. 2

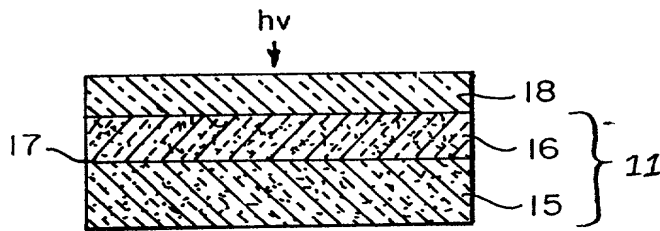


FIG.3

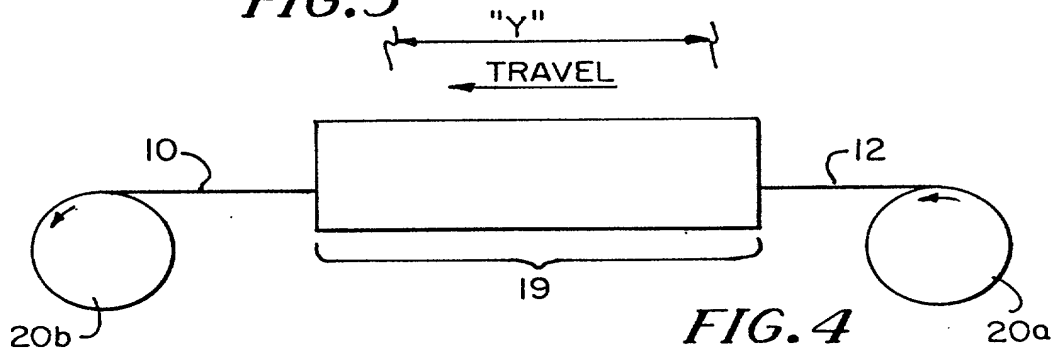


FIG. 4

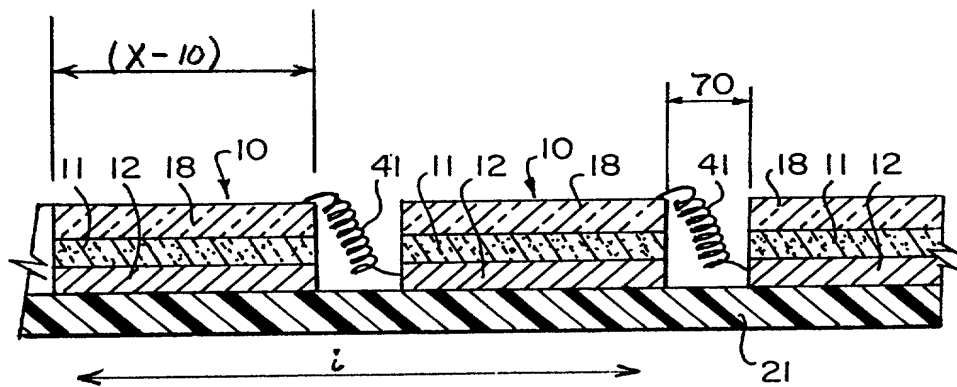


FIG. 5

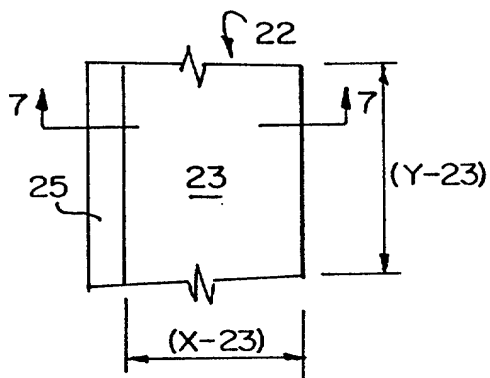


FIG. 6

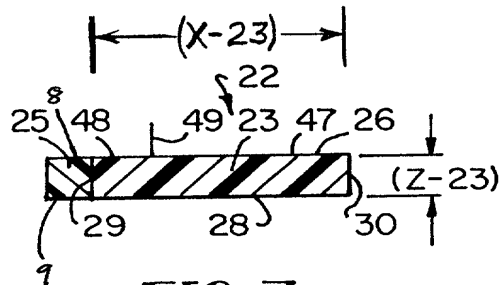


FIG. 7

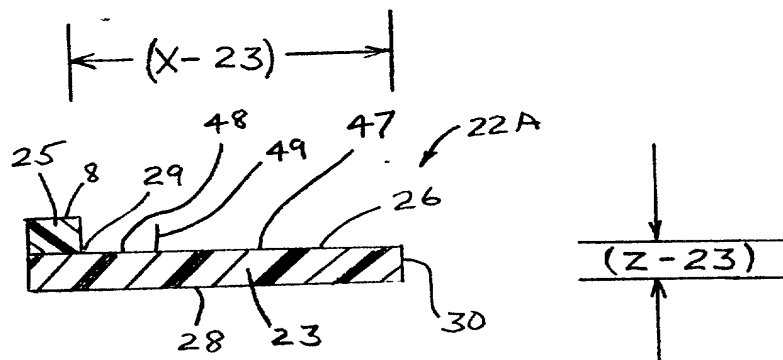


FIG. 7A

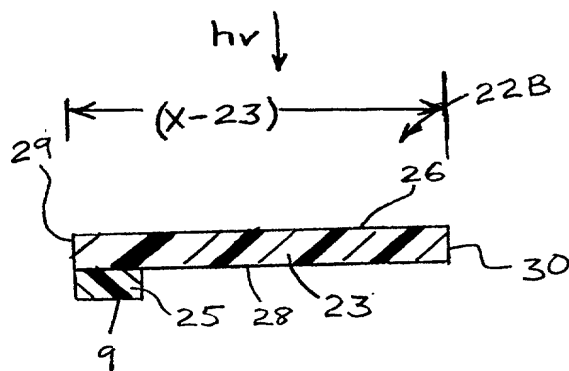


FIG. 7B



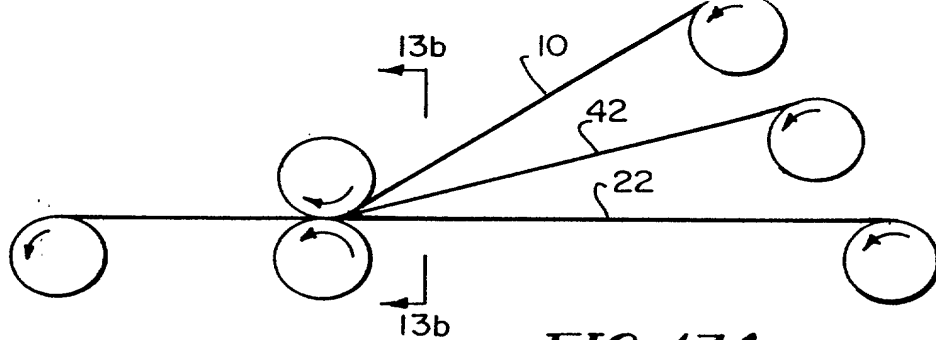


FIG. 13A

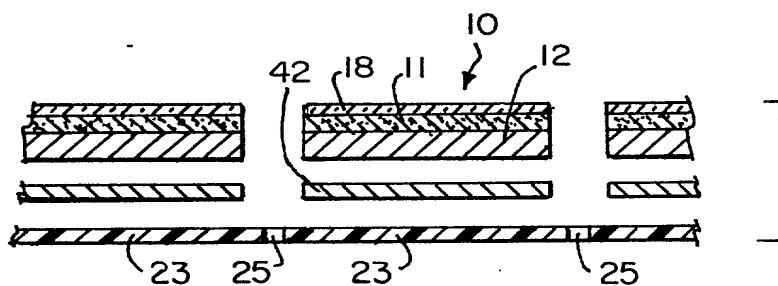


FIG. 13B

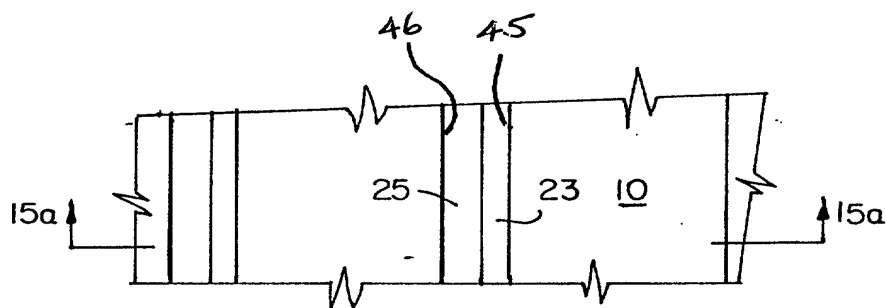


FIG. 14A

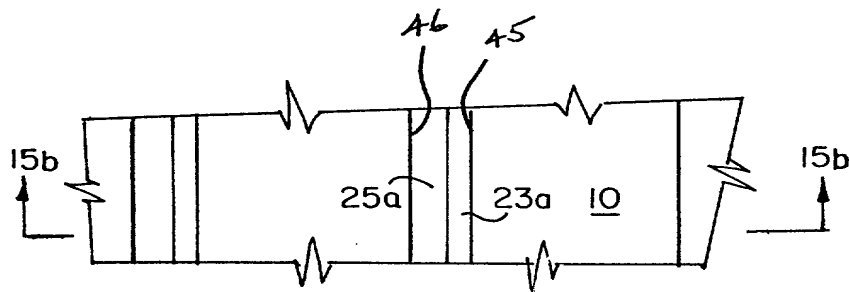


FIG. 14B

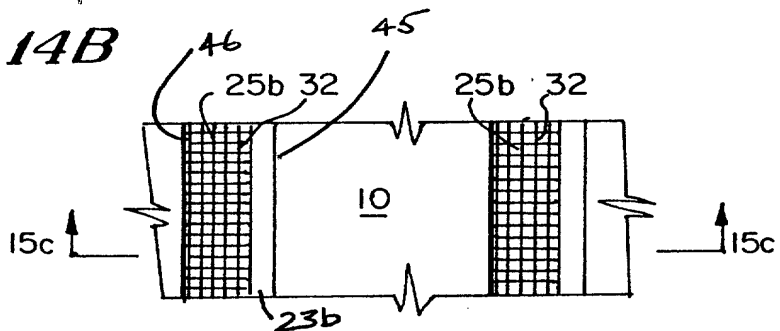


FIG. 14C

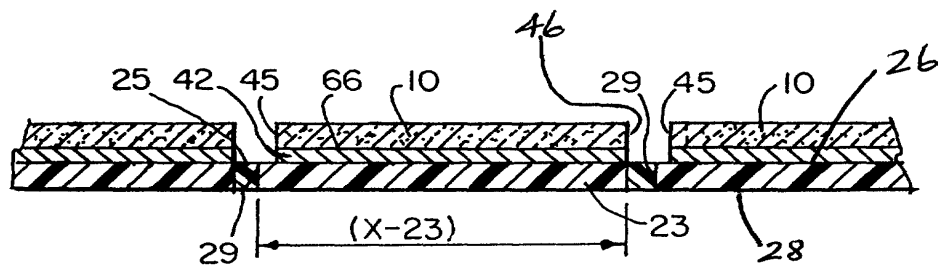


FIG. 15A

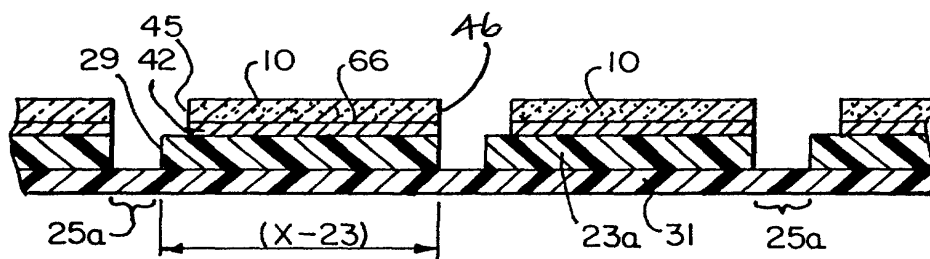


FIG. 15B

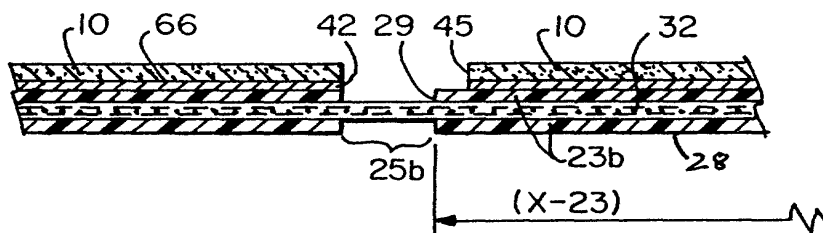


FIG. 15C

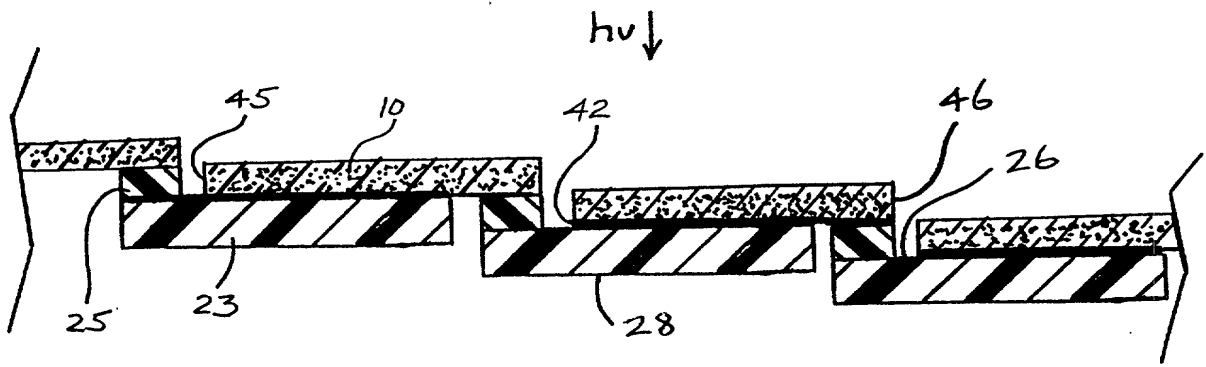


FIG. 15D

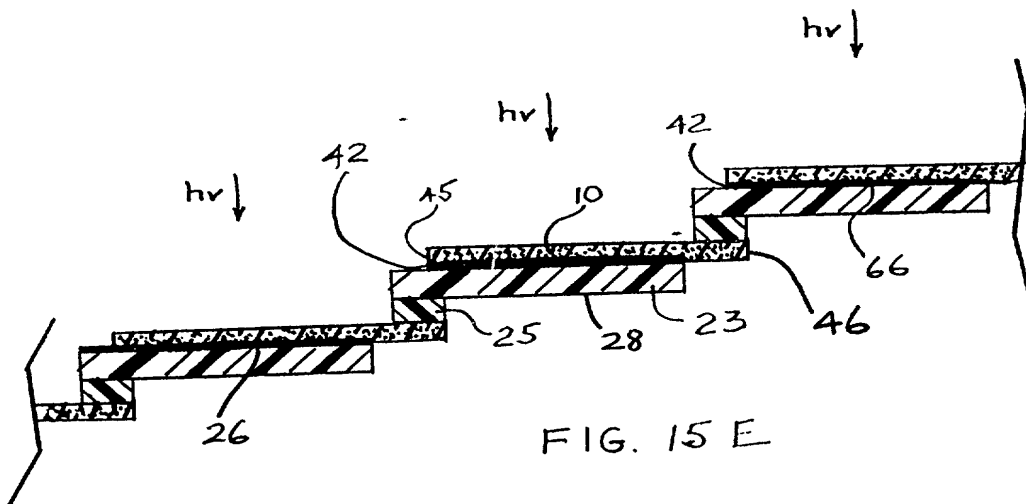


FIG. 15E

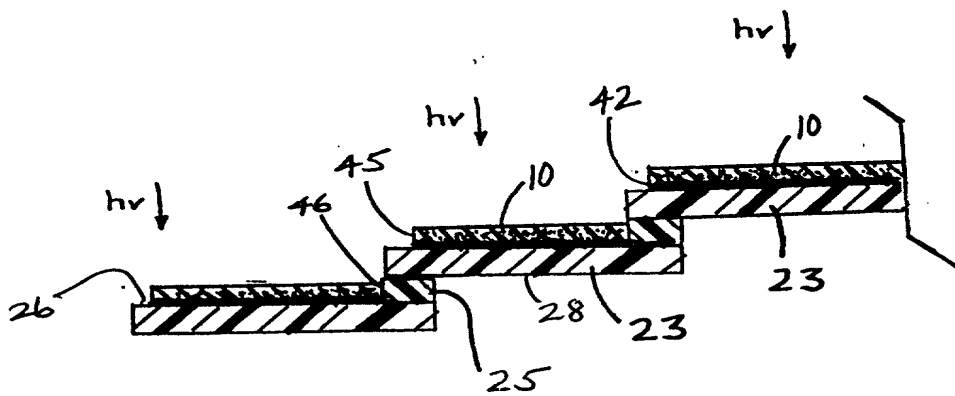


FIG. 15F

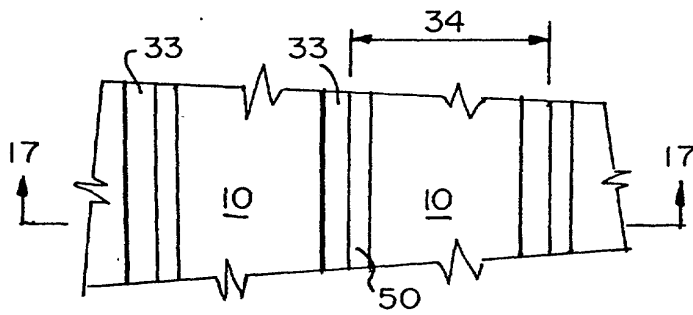


FIG. 16

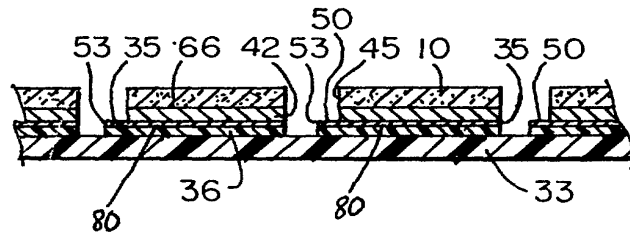


FIG. 17

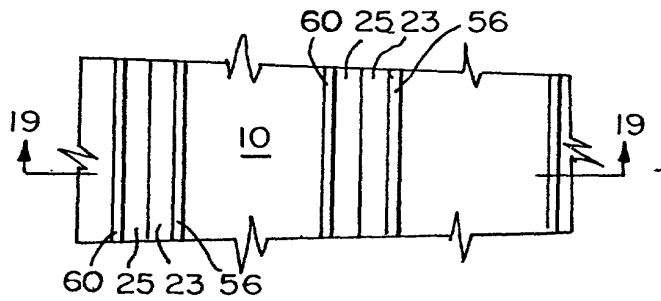


FIG. 18

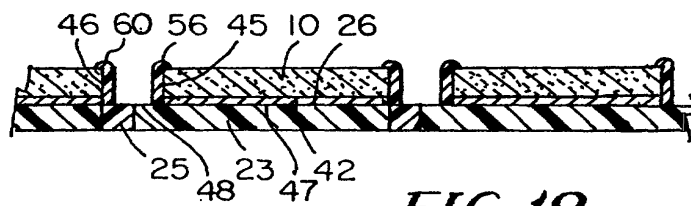


FIG. 19

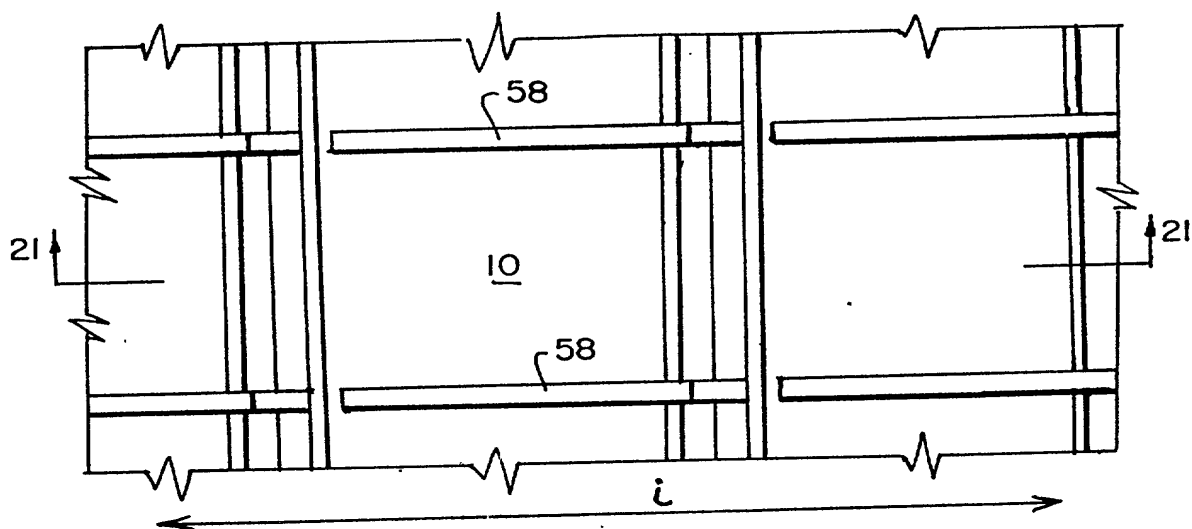


FIG. 20

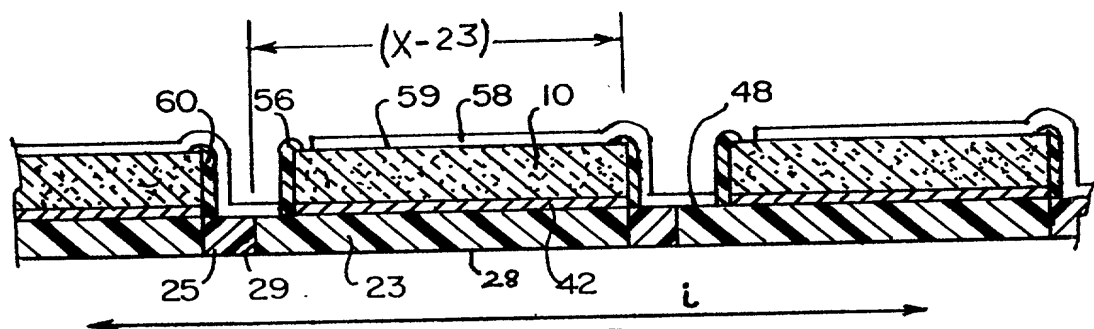


FIG. 21

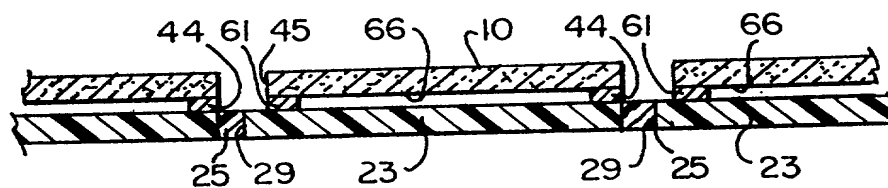


FIG. 22

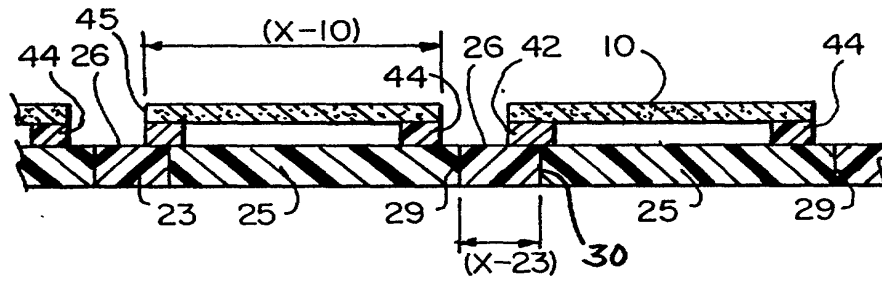


FIG.23

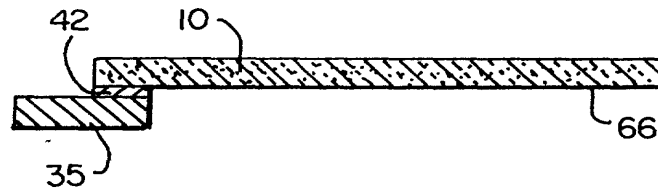


FIG.24

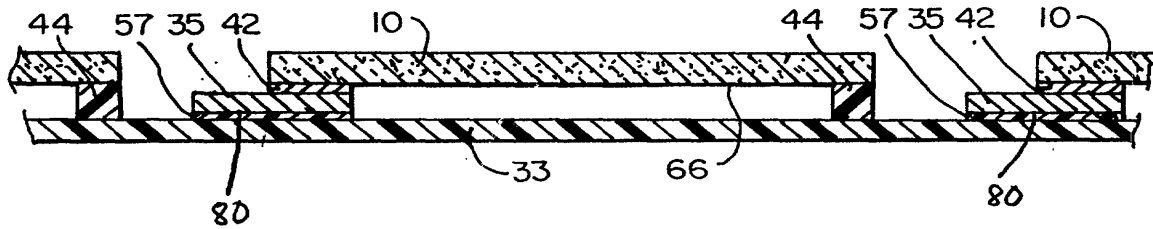


FIG.25

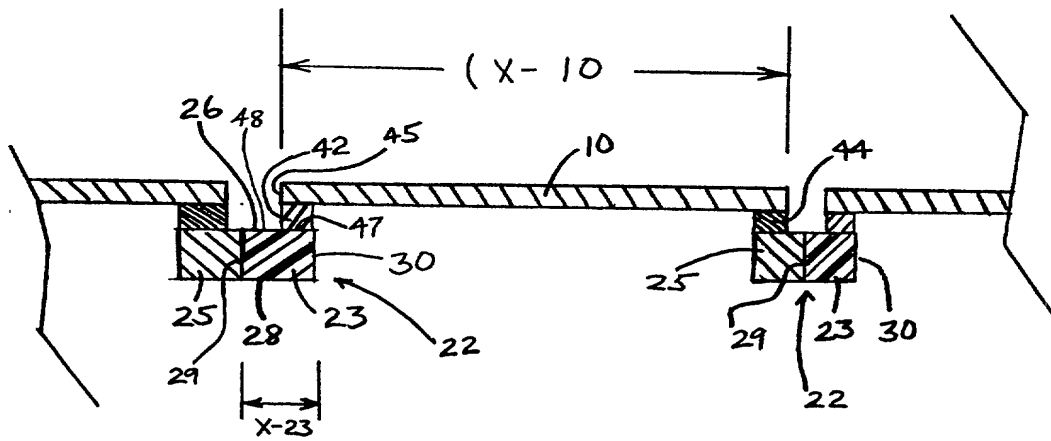


FIGURE 26

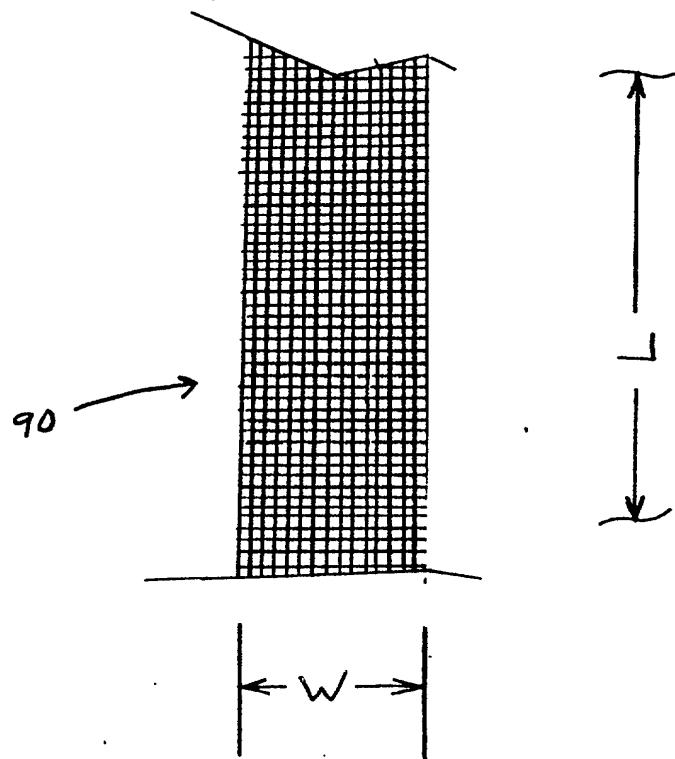


FIGURE 27

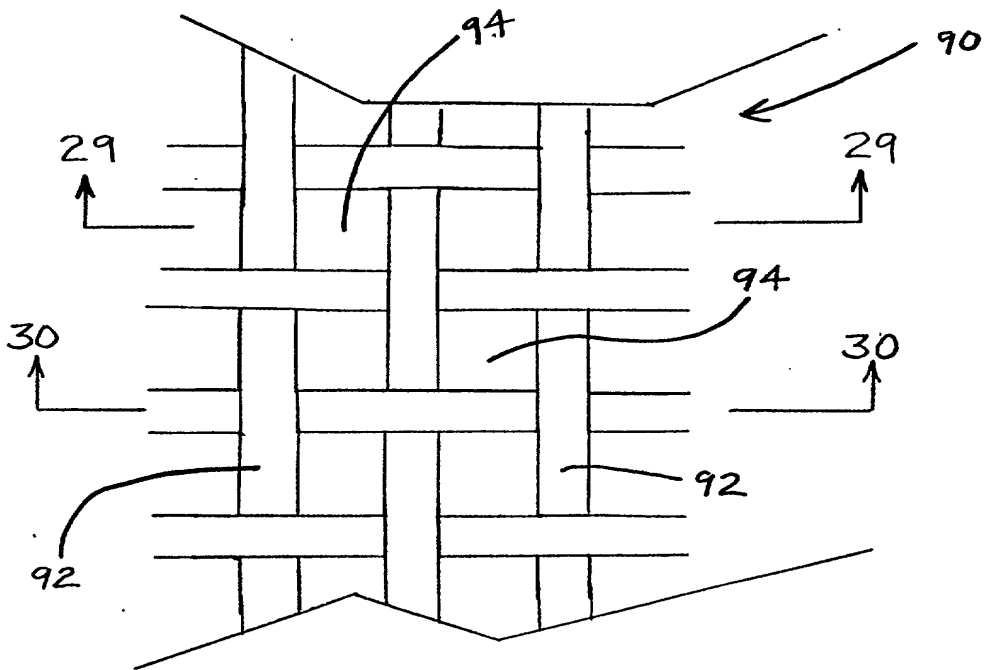


FIGURE 28

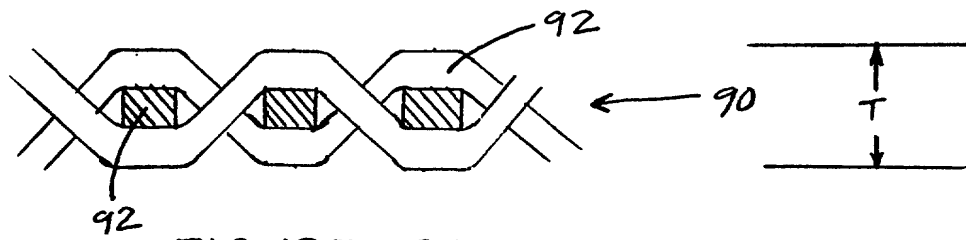


FIGURE 29

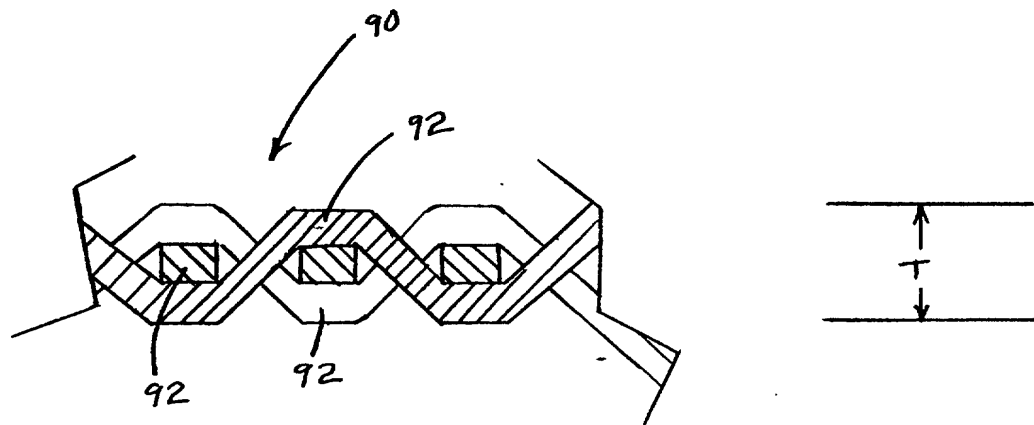


FIGURE 30



FIGURE 31

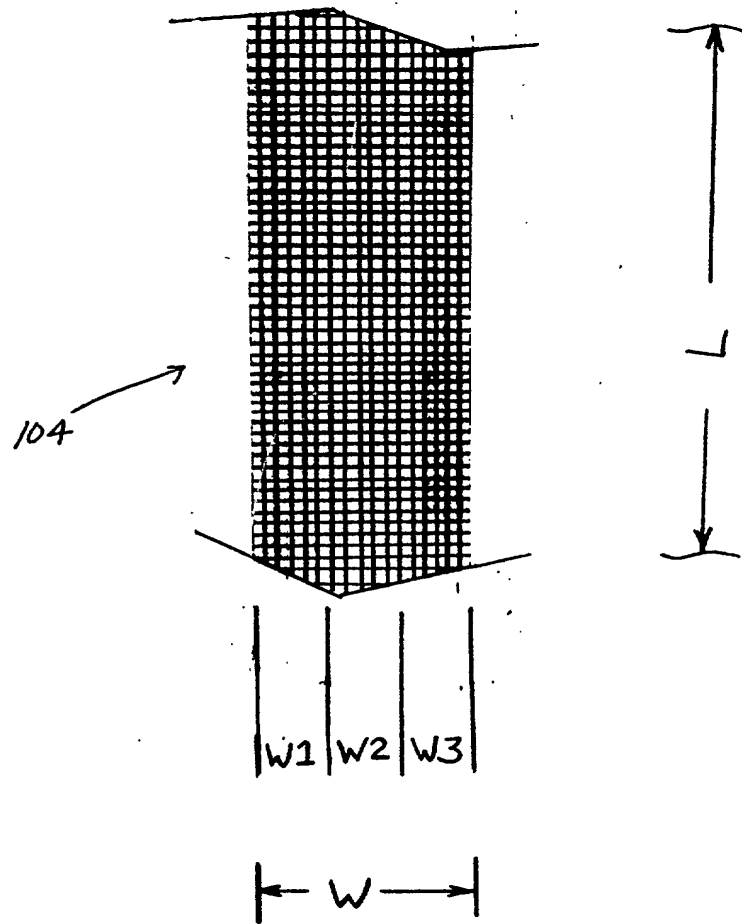


FIGURE 32

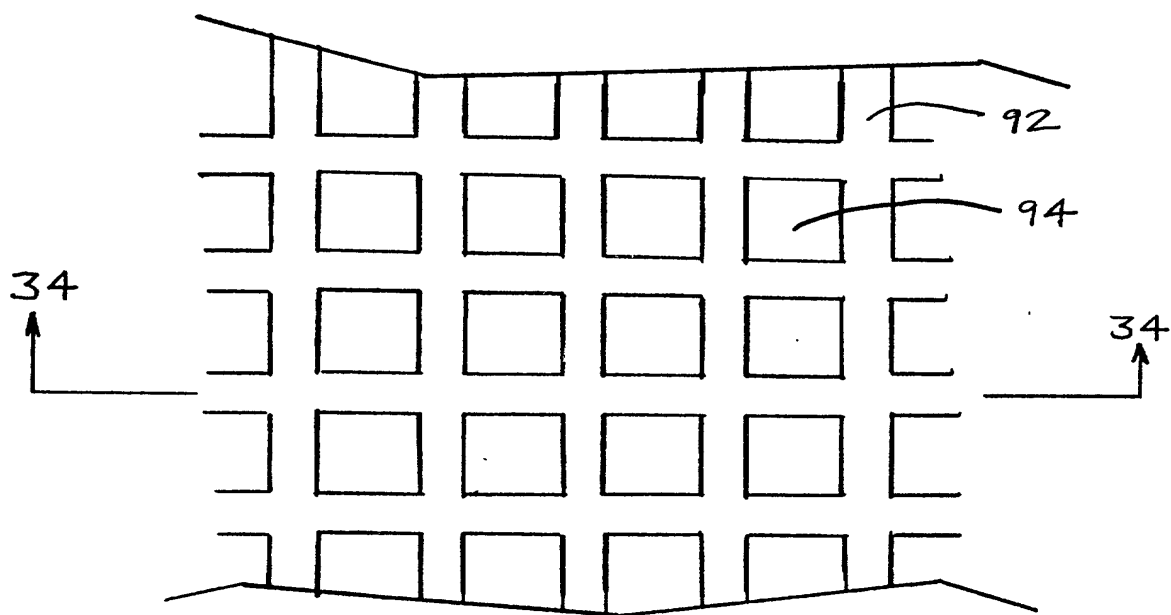


FIGURE 33

FIGURE 34

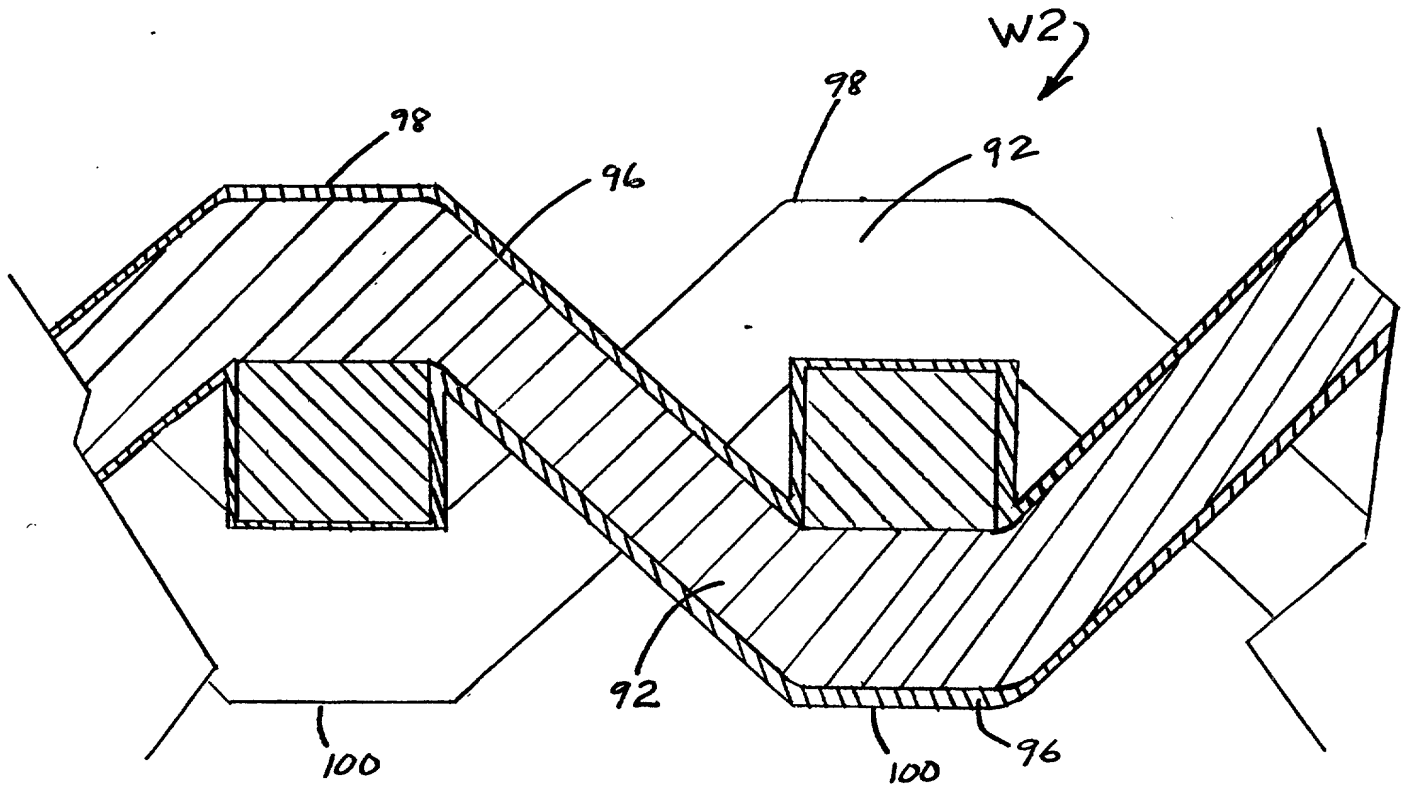


FIGURE 34

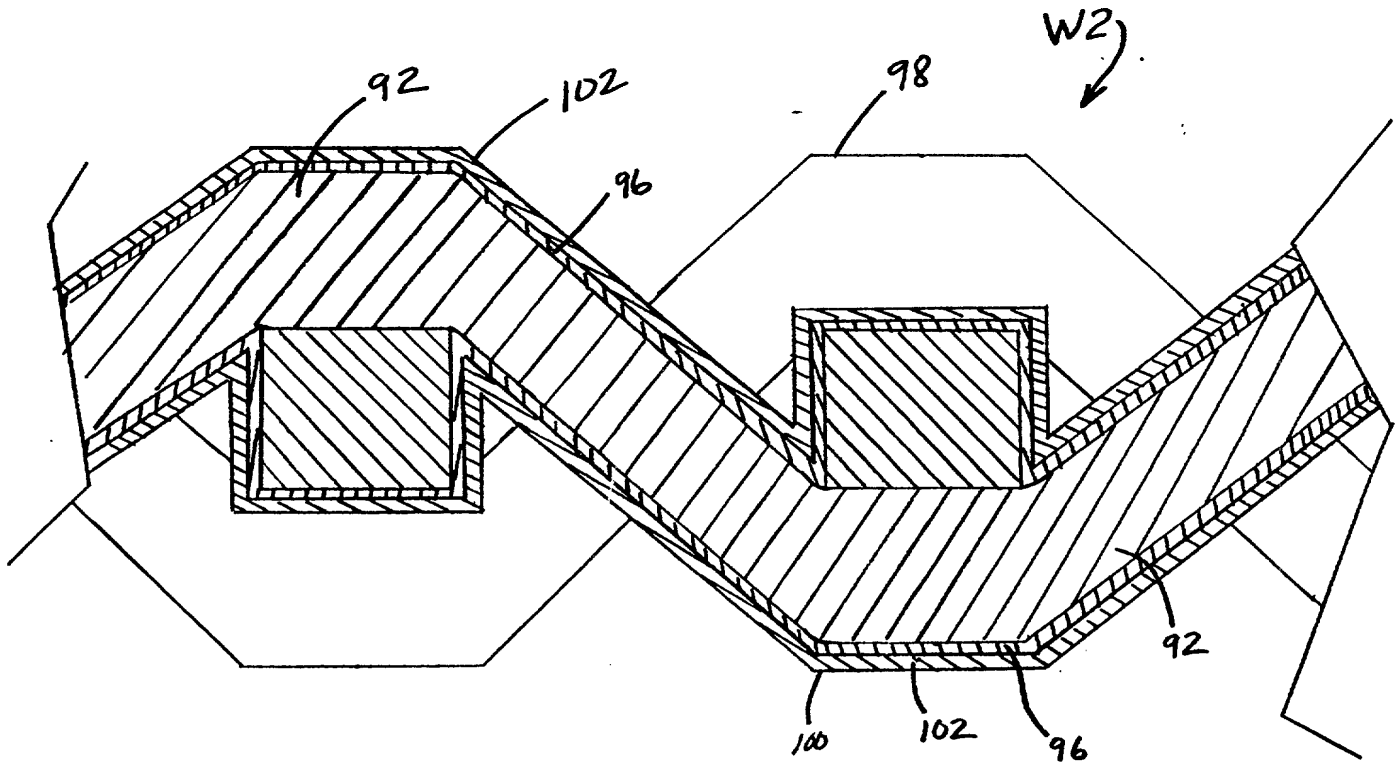


FIGURE 35

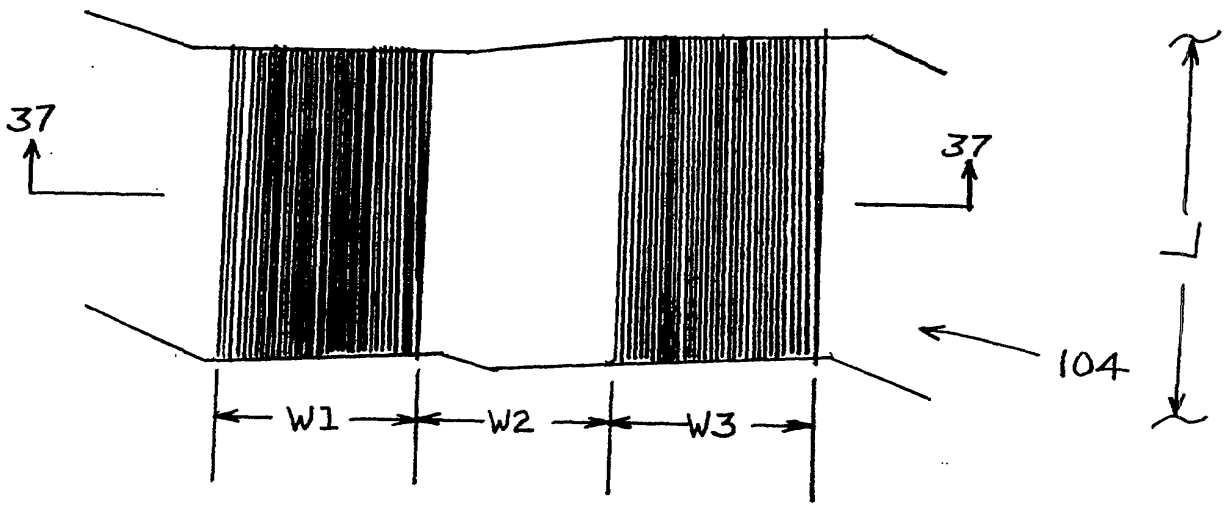


FIGURE 36

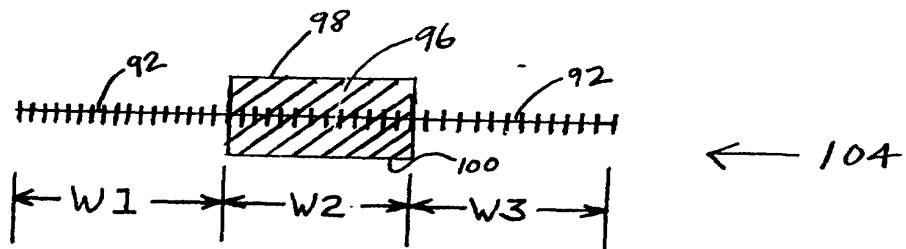


FIGURE 37A

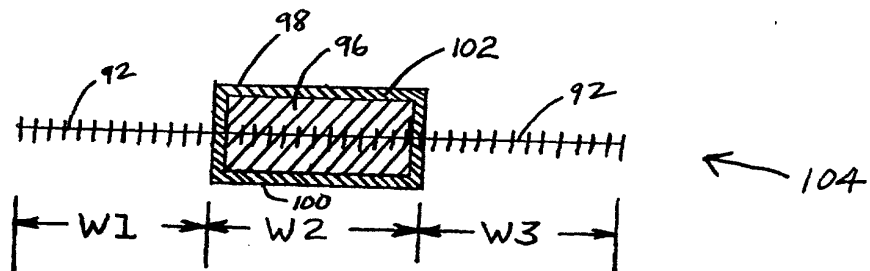


FIGURE 37B

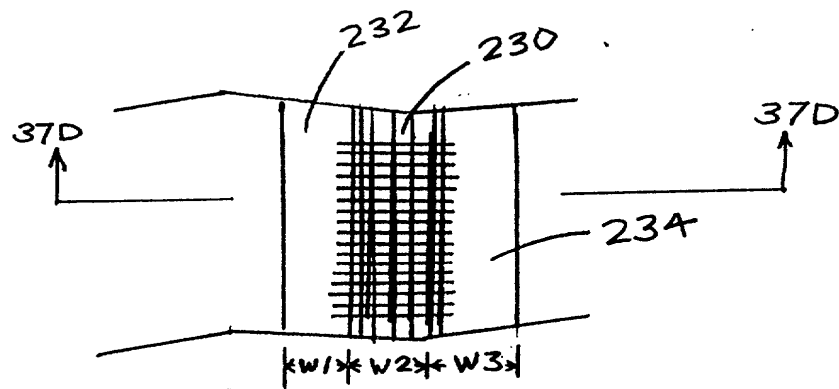


FIG 37C

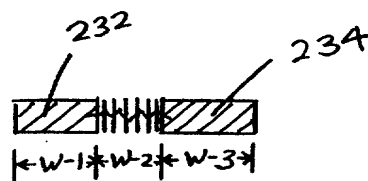


FIG 37D

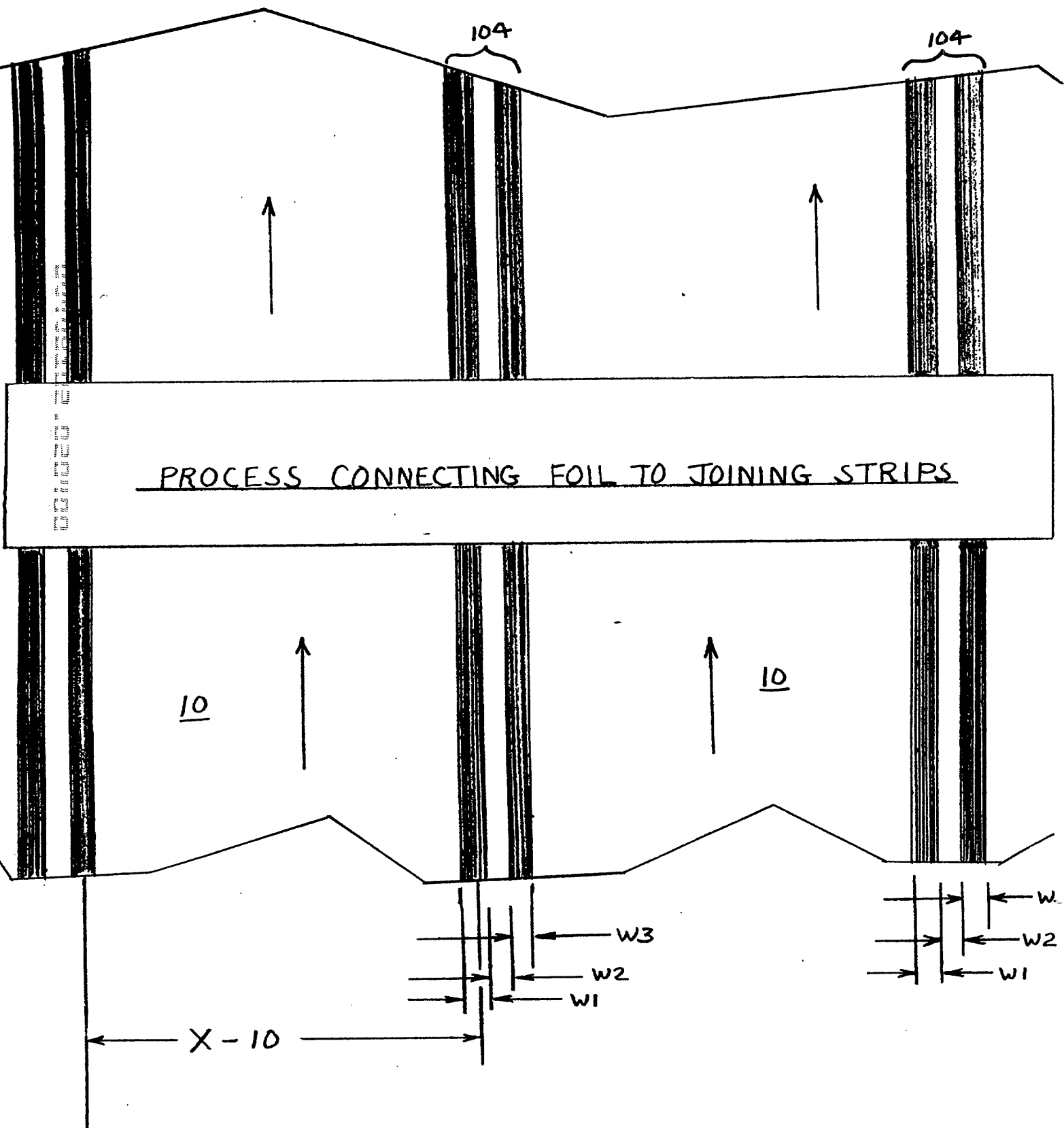


FIGURE 38

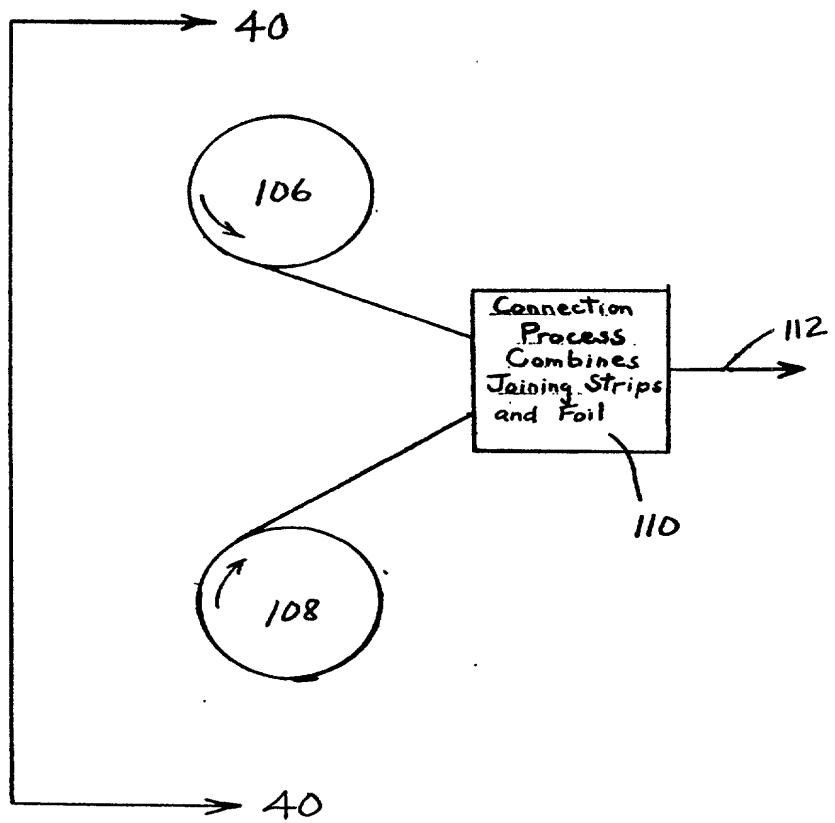


FIGURE 39

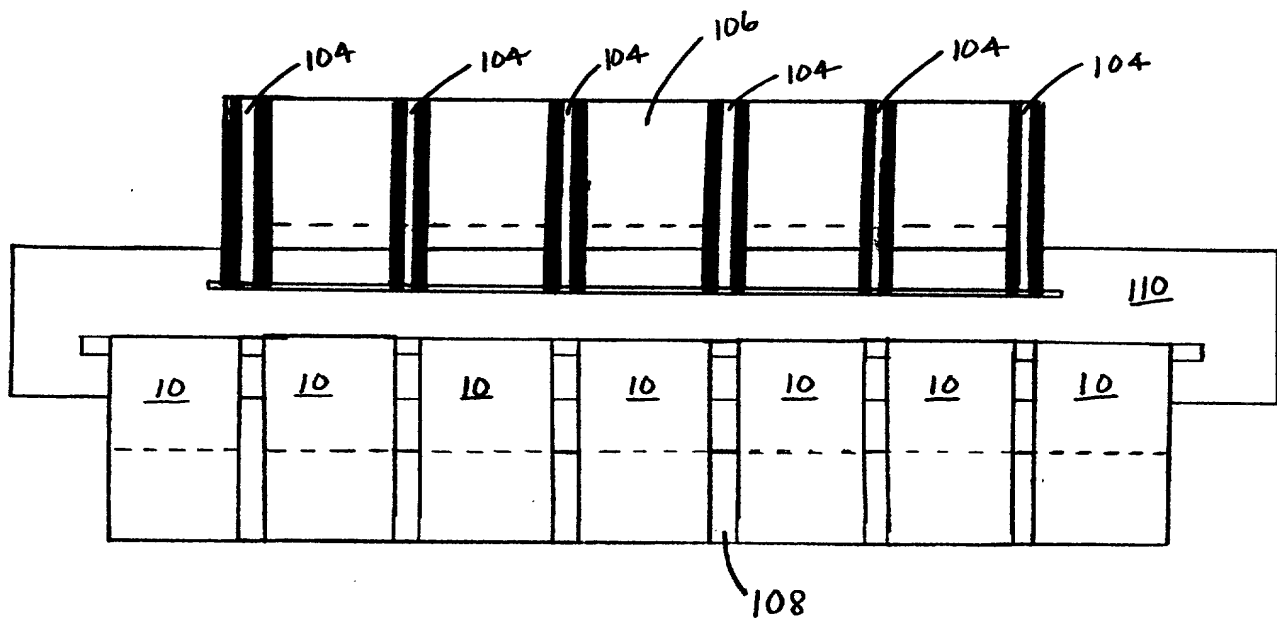


FIGURE 40

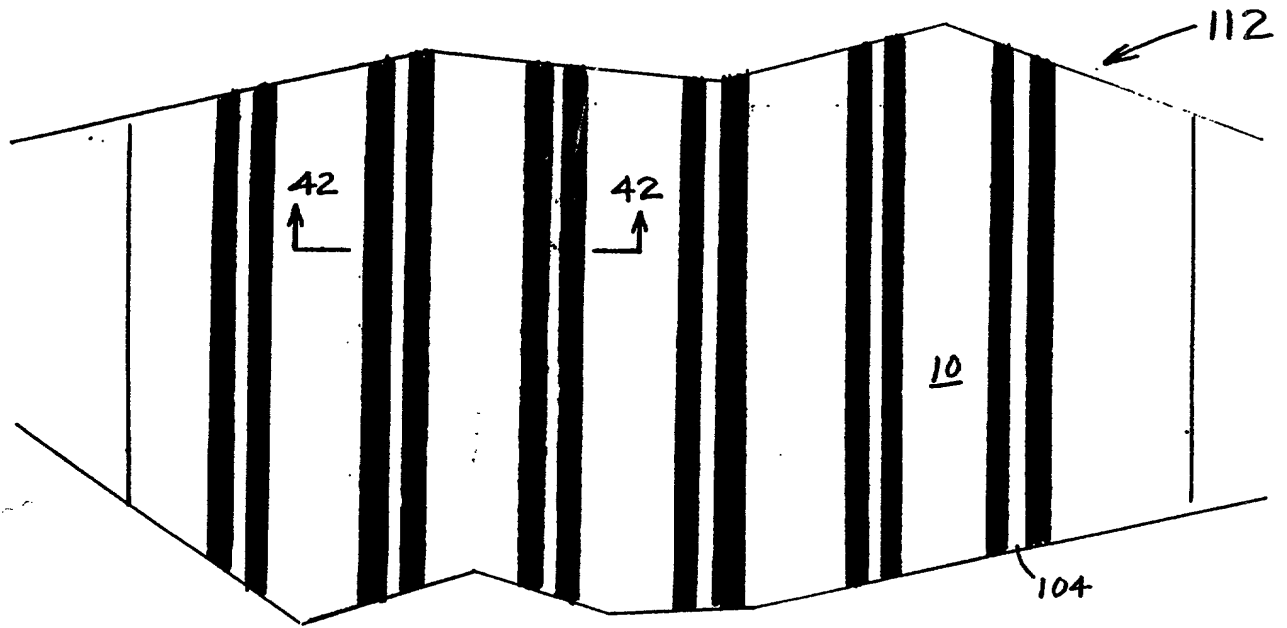


FIGURE 41.

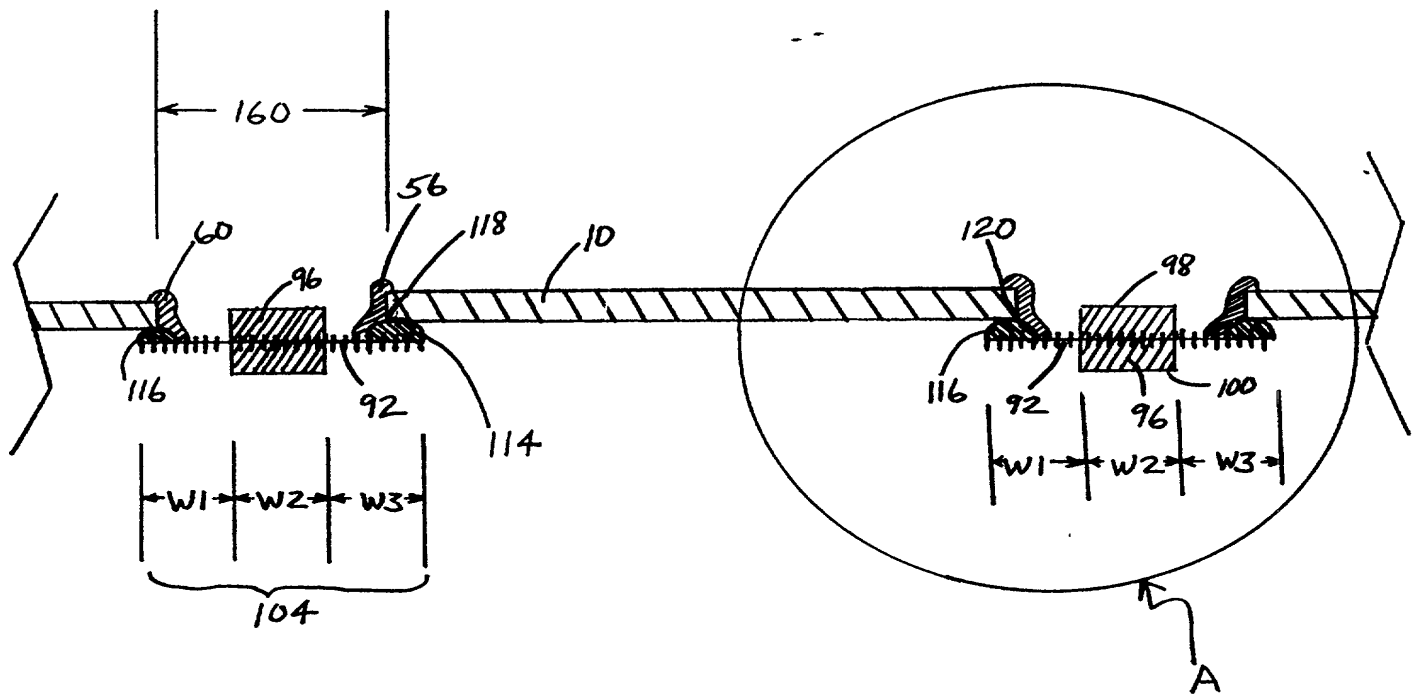


FIGURE 42 A

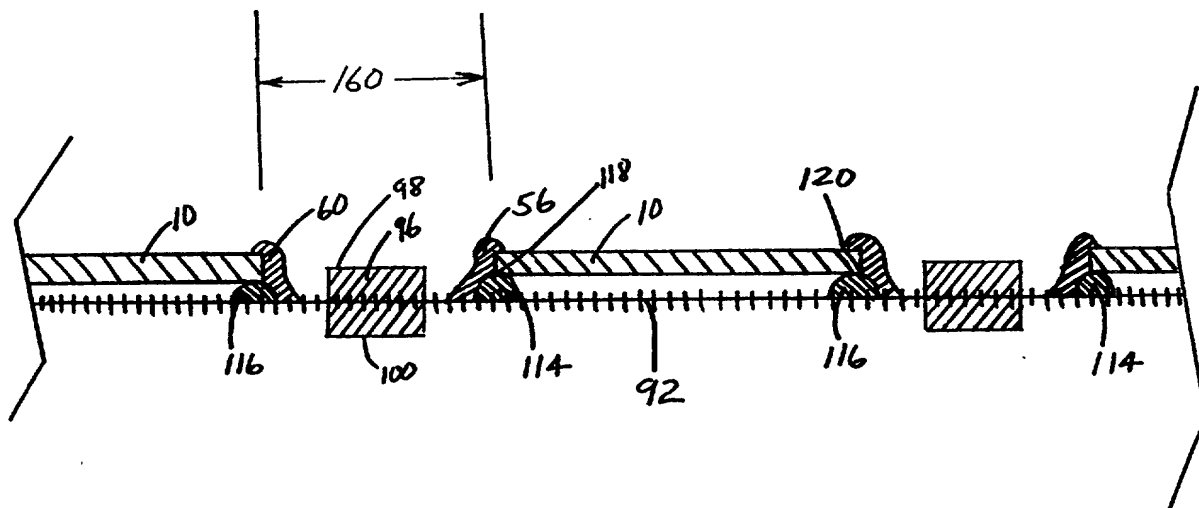


FIGURE 42B

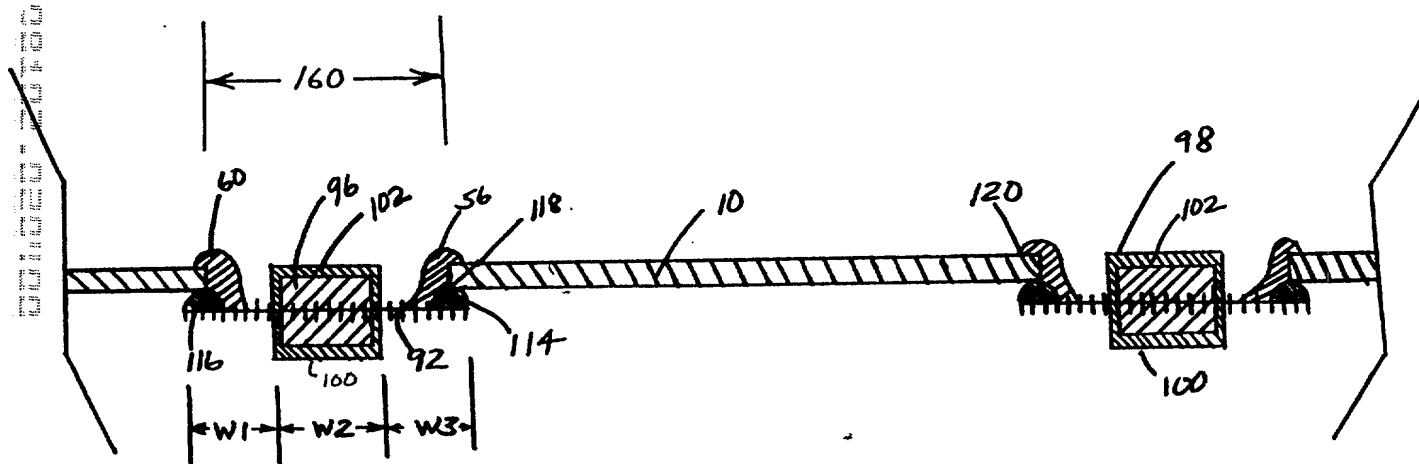


FIGURE 42C

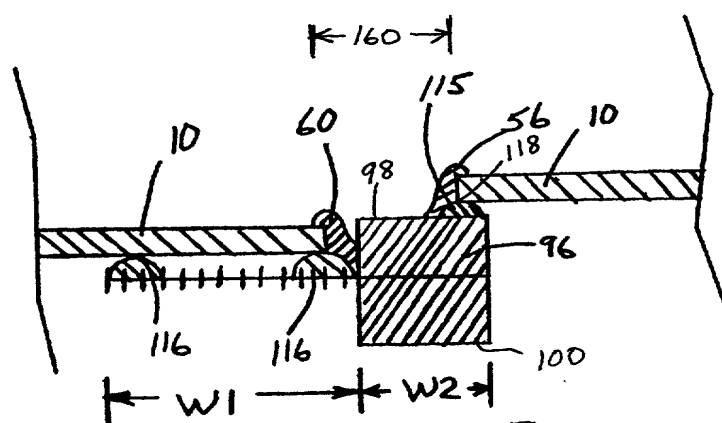


FIG. 42 D

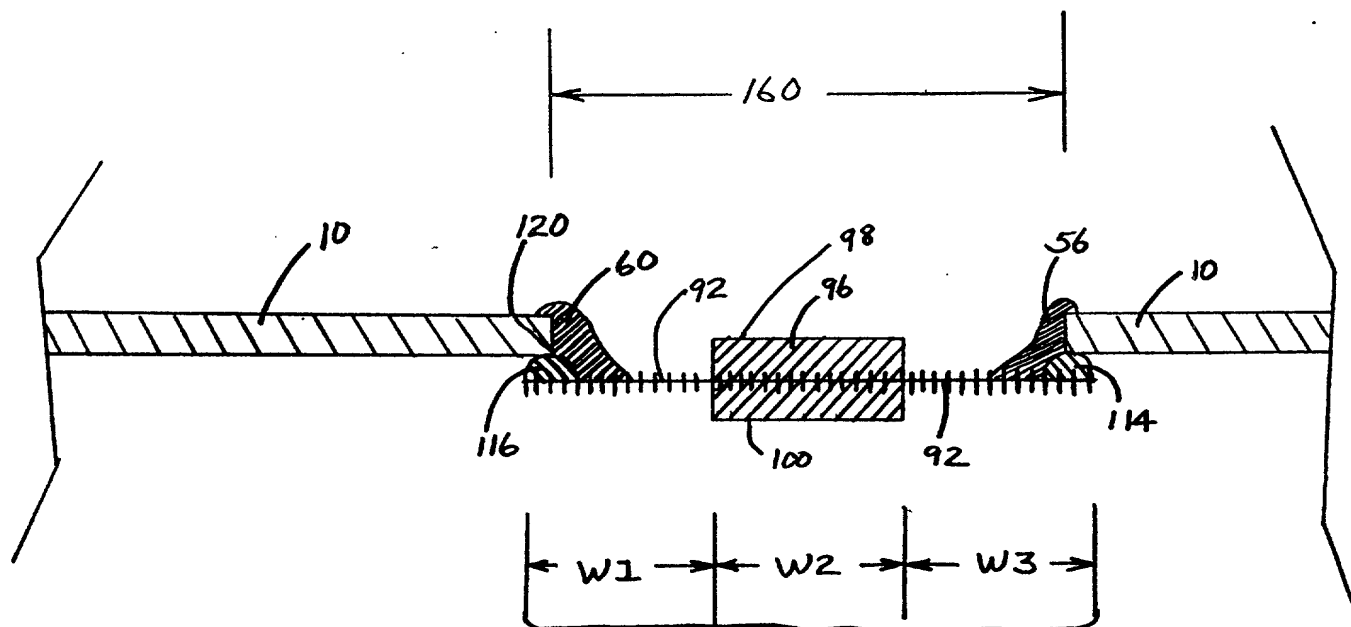


FIGURE 43

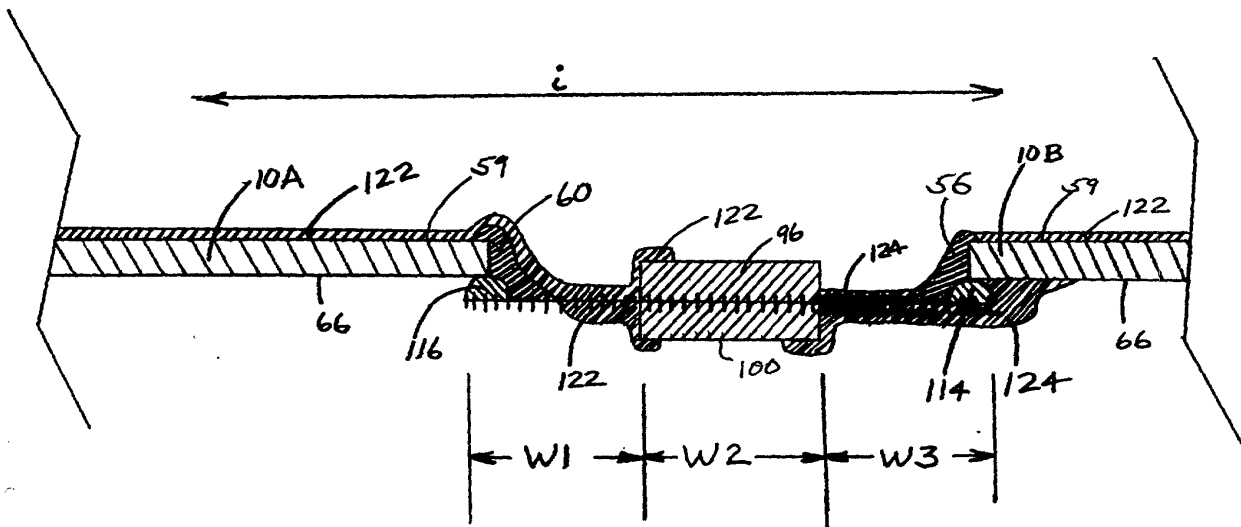


FIGURE 44

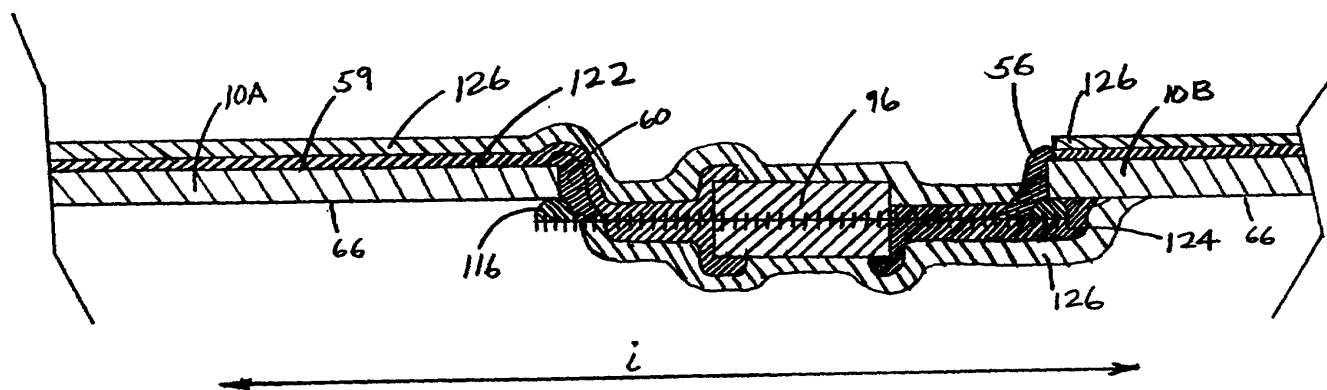


FIGURE 45

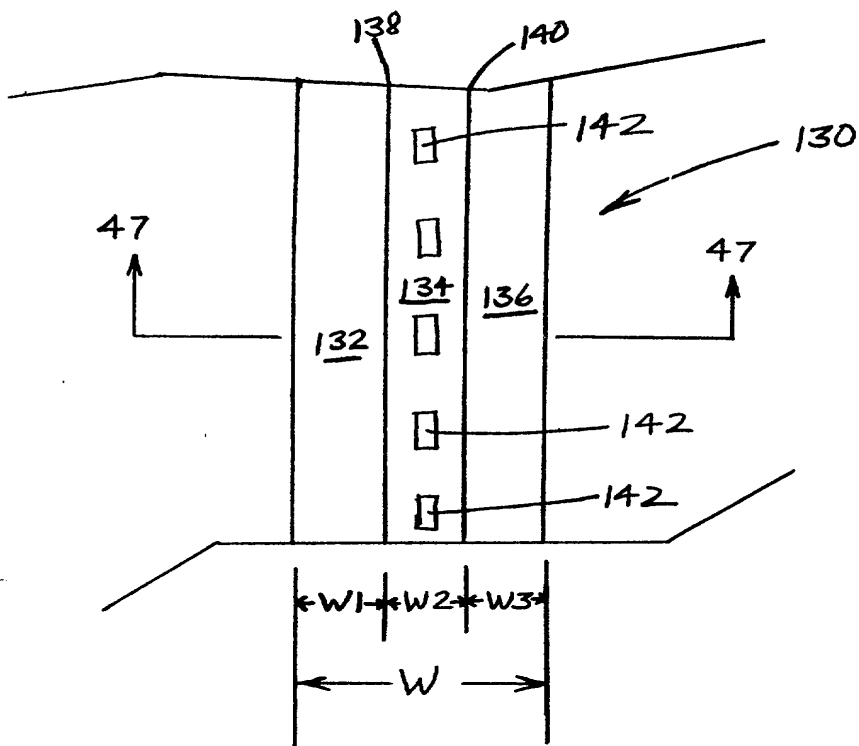


FIGURE 46

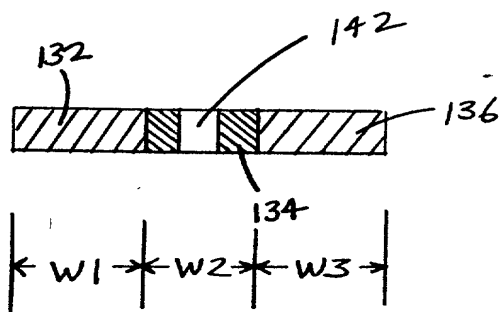


FIGURE 47

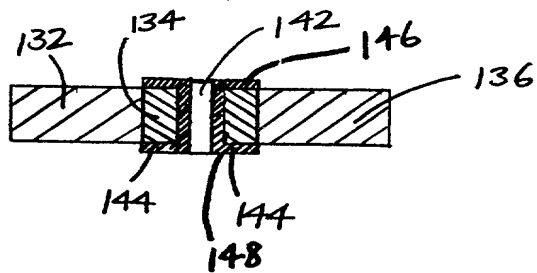


FIGURE 48

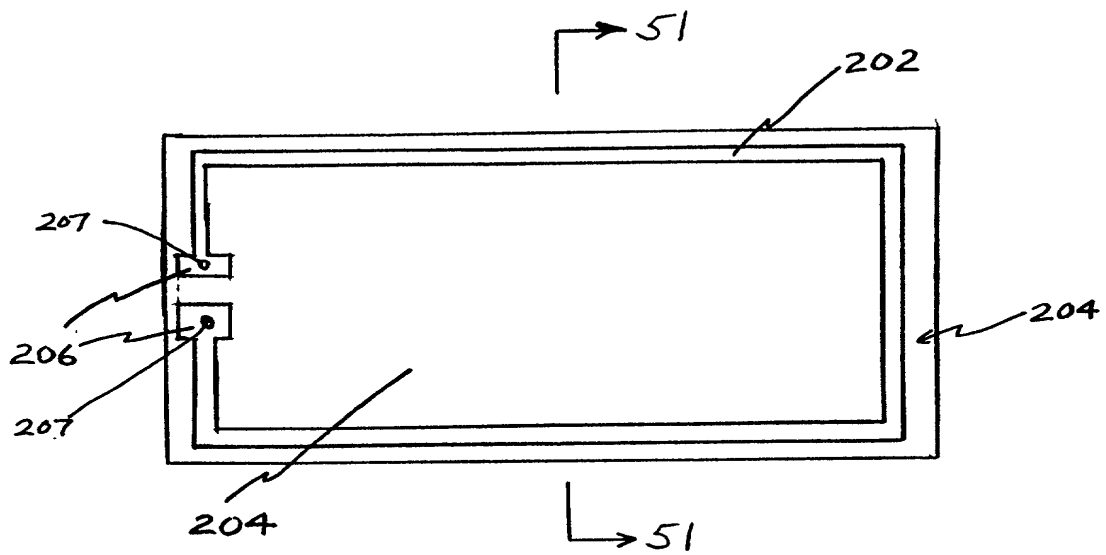


FIG. 50

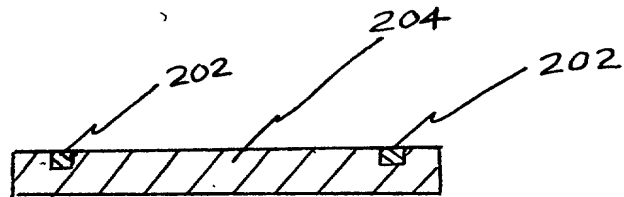


FIG. 51

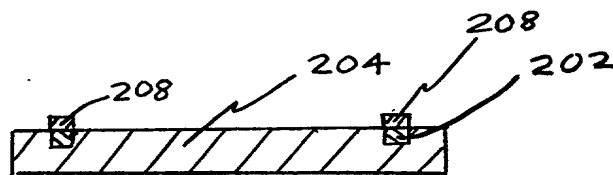


FIG. 52

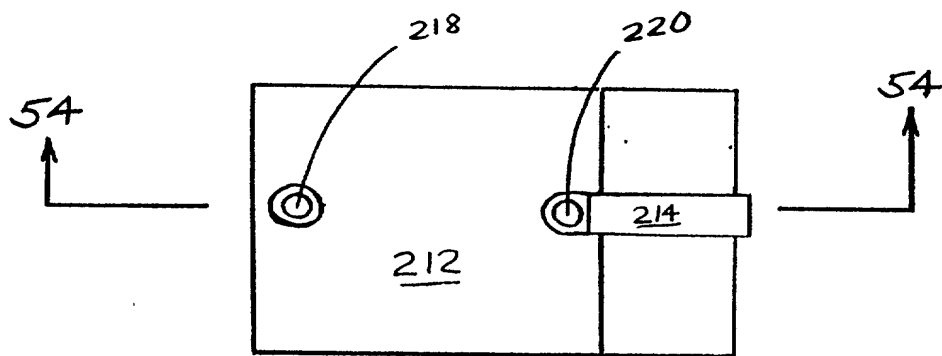


FIG. 53

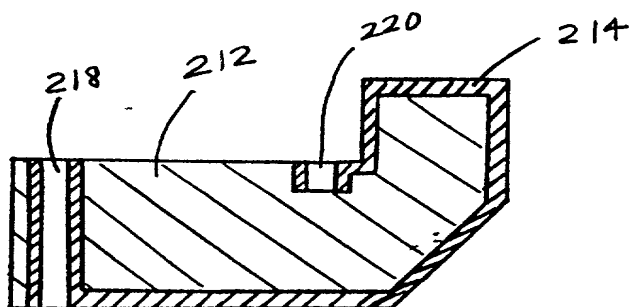


FIG. 54

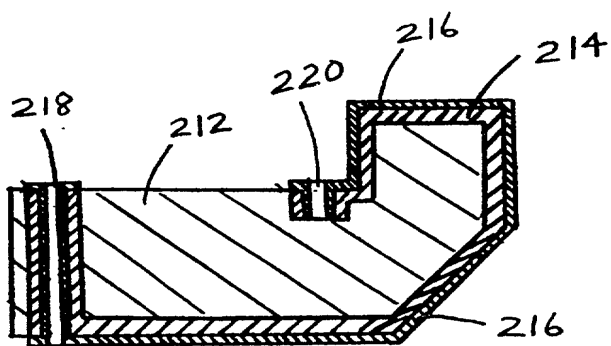


FIG. 55

DECLARATION FOR PATENT APPLICATION

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled SUBSTATE STRUCTURES FOR INTEGRATED SERIES CONNECTED PHOTOVOLTAIC ARRAYS AND PROCESS OF MANUFACTURE OF SUCH ARRAYS, the specification of which

(check one) ☒ is attached hereto.
☐ was filed on _____ as
Application Serial No. _____
and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the Patent Office all information known to me to be material to patentability as defined in 37 C.F.R. 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed

| (Number) | (Country) | (Day/Month/Year Filed) | <input type="checkbox"/> Yes | <input type="checkbox"/> No |
|----------|-----------|------------------------|------------------------------|-----------------------------|
|----------|-----------|------------------------|------------------------------|-----------------------------|

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose to the Patent Office all information known to me to be material to patentability as defined in 37 C.F.R. 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application: _

| | | |
|------------|---------------|-----------------------|
| 08/441,552 | May 15, 1995 | U.S. Patent 5,547,516 |
| 08/683,061 | July 16, 1996 | U.S. Patent 5,735,966 |
| 08/967,962 | Nov. 12, 1997 | Pending |

| (Application Serial No.) | (Filing Date) | (Status) |
|--------------------------|---------------|----------|
|--------------------------|---------------|----------|

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Address all correspondence to: Daniel Luch
17161 Copper Hill Drive
Morgan Hill, CA 95037

File No. _____

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name inventor:

DANIEL LUCH

Inventor's signature:

Daniel Luch

Date:

02/04/00

Residence:

Morgan Hill, California

Citizenship:

United States

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